## E.G.S. PILLAY ENGINEERING COLLEGE

(Autonomous)

Approved by AICTE, New Delhi | Affiliated to Anna University, Chennai Accredited by NAAC with "A" Grade | Accredited by NBA (CSE, EEE, MECH)

NAGAPATTINAM – 611 002



## **B.E.** Electronics and Communication Engineering

## **Full Time Curriculum and Syllabus**

**Second Year – Fourth Semester** 

Course	Course Name	L	Т	P	C	Maximum		Marks	
Code	Course Name	L	1	r	C	CA	ES	Total	
Theory Cours	se								
1701MA403	Probability and Random Processes	3	2	0	4	40	60	100	
1702EC402	Signals and Systems	3	2	0	4	40	60	100	
1702EC403	Analog Integrated Circuits	3	0	0	3	40	60	100	
1702EC404	Microprocessors and Microcontrollers	3	0	0	3	40	60	100	
1702EC405	Transmission Lines and Waveguides	3	0	0	3	40	60	100	
1702EC406	Control Systems	3	0	0	3	40	60	100	
Laboratory (	Laboratory Course								
1702EC451	Analog Integrated Circuits Laboratory	0	0	4	2	50	50	100	
1702EC452	Microprocessors and Microcontrollers Laboratory	0	0	4	2	50	50	100	
1704GE451	Life Skills: Verbal Ability	0	0	2	-	100	-	100	

L - Lecture | T - Tutorial | P - Practical | C - Credit | CA - Continuous Assessment | ES - End Semester

1701MA401	PROBABILITY AND RANDOM PROCESSES (B.E- ECE )	1 3	T 2	P 0	C 4
Course Obje			<u> </u>	Ü	
<b>3</b>	To analyze the concepts of probability, random variables and distribution.	ributio	on fun	ctions	
	2. To acquire skill in handling situation with more than one random				
	function.				
	3. To analyze the concept of signals and system.				
Unit I	PROBABILITY			9+3H	lours
	Theorems on Probability- Conditional Probability – Baye's Theorem- Discrete	te and			
	bles – Moments – Moment generating functions –Real Time Problems				
Unit II	ONE DIMENSIONAL RANDOM VARIABLE		9	9+3 H	ours
Discrete Distr	ibutions: Binomial, Poisson, Geometric - Continuous Distributions: Uniform	n, Exp	onent	tial,	
Normal distril	outions- Application of Distribution in Engineering Problems				
Unit III	TWO - DIMENSIONAL RANDOM VARIABLES			9+3 H	
	ions - Marginal and conditional distributions - Covariance - Correlation an	d Line			
Unit IV	MARKOV PROCESSES AND MARKOV CHAINS			9+3 H	
Classification	- Stationary process - Markov process - Markov chains - transition probab	ilities	– Lir	niting	
distributions -	- Poisson process.				
Unit V	SPECTRAL DENSITIES AND LINEAR SYSTEMS WITH RANDO INPUTS	M	!	9+3 H	lours
Auto correlati	on-cross correlation-power spectral density-cross spectral density-Properties	s-Wie	ner-K	hintch	ine
relation-relati	onship between cross power spectrum and corre lation function. Linear time	e inva	riant s	ystem	1-
	er function-Linear system with random inputs-White noise.			•	
	Tot	al:	45 -	+ 15 H	lours
Further Read					10415
	Probabilistic manner which evolve with time				
	Discrete time Markov chains in modeling Electronic systems.				
Course Outc					
	After completion of the course, Student will be able to				
	1. To apply basic probability techniques to analyze the performance of El	ectro	nic sys	stems.	(K3)
	2. To apply standard distributions in describing real life phenomena.(K3)				
	3. To solve problems involving more than one random variable.(K3)				
_	4. To apply probability technique which evolve with respect to time.(K3)		****		
D. C	5. To interpret the response of random input to linear time invariant syste	ms. (	K3)		
References:	The Fundamentals of Applied Duckshillter and Dandam Duckshiller	1 a4 T.	dia- T	) o==::-	+
2007	Ibe, Fundamentals of Applied Probability and Random Processes, Elsevier,			Keprin	τ,
	ross and C.M. Harris, Probability and random processes, WileyStudent edition				
	les. P.Z., "Probability, Random Variables and Random Signal Principles", T	ata M	c Gra	w Hill	i, 4th
	on, New Delhi, 2002.		X 7°1	т 1'	D /
	s. R.D. and Goodman. D.J., "Probability and Stochastic Processes", 2nd Edit	ion, \	wney	ındıa	rvt.
	Bangalore, 2012.  H., and Woods. J.W., "Probability and Random Processes with Application	ne to 9	Signal		
	essing", 3rd Edition, Pearson Education, Asia, 2002.	15 10 1	oigiiai		
	r. S.L. and Childers. D.G., "Probability and Random Processes with Applications."	ations	to Sic	mal	
	essing and Communications", Academic Press, 2004.	10113	W 1018	>11u1	
	indiastudychannel.com				
	ac.in/courses/111105035, www.nptelvideos.in/2012/11/Mathematics.html				
	.learnerstv.com/Free-maths-video lectures - ltv348-page1.html				
,	Transfer to main trace rectares 100 page main				

1702EC402	SIGNALS AND SYSTEMS	L 3	T 1	P 0	C 4
Course Objec	tives:	3	1	U	
9	1. To understand the basic properties of Signals and Systems and the	vari	ous n	netho	ds of
	Classification				
	2. Tolearn Laplace Transform & Fourier transform and their proper	ties			
	3. To know Z transform & DTFT and their properties.				
	4. To characterize LTI systems in the Time domain and various Tra	ansfo	rm d	omaiı	ıs
Unit I	CLASSIFICATION OF SIGNALS AND SYSTEMS			+3 H	
	of Signals- Continuous time signals - Discrete time signals - Periodic and				
	signals - Energy and power signals -Deterministic and random signals -Co				
	signals. Classification of Systems: Continuous time systems- Discrete tir				
-	Invariant system – causal system - BIBO system - Systems with and with	hout	mem	ory -	LTI
	ication of Systems			. 2 11	
Unit II	ANALYSIS OF CONTINUOUS TIME SIGNALS analysis-spectrum of Continuous Time (CT) signals- Fourier and Laplace	Tuon		+3 H	
Signal Analysi		Han	STOTII	18 111 1	∪1
Unit III	LTI CT SYSTEM		9	+3 H	niirs
	nse - Frequency response – Convolution Integral - Analysis and characteristics	acter			
	Laplace transform Solution of Differential equation with initial cond				
	ero input response.				~
Unit IV	ANÂLYSIŜ OF DISCRETE TIME SIGNALS		9	+3 H	ours
Baseband Sar	npling - DTFT – Properties of DTFT - Z Transform – Properties of Z Tra	nsfo	m –I	nvers	e Z
transform					
Unit V	LTI DISCRETE TIME SYSTEMS			+3 H	
	nse - Convolution sum- Analysis and characterization of DT system	usin	gΖ	trans	form
Difference Equ					
=========	nations-Block diagram				
	nations-Block diagram  Total	:	45+	15 H	ours
Further Read	Total:				
	Totals  ing:  Programs using mathematical computing tool for CT and DT system and				
Further Read	ing: Programs using mathematical computing tool for CT and DT system and ZT				
	Totals  ing:  Programs using mathematical computing tool for CT and DT system and ZT  mes:				
Further Read	Totals  ing:  Programs using mathematical computing tool for CT and DT system and ZT  mes:  After completion of the course, Student will be able to				
Further Read	Totals  Ing:  Programs using mathematical computing tool for CT and DT system and ZT  Totals  Programs using mathematical computing tool for CT and DT system and ZT  Totals  After completion of the course, Student will be able to  1. Analyze the properties of signals & systems				
Further Read	Totals  ing:  Programs using mathematical computing tool for CT and DT system and ZT  mes:  After completion of the course, Student will be able to  1. Analyze the properties of signals & systems  2. Apply Laplace transform, Fourier transform in signal analysis	alysi	s usir		
Further Read	Totals  Ing:  Programs using mathematical computing tool for CT and DT system and ZT  Totals  Programs using mathematical computing tool for CT and DT system and ZT  Totals  After completion of the course, Student will be able to  1. Analyze the properties of signals & systems  2. Apply Laplace transform, Fourier transform in signal analysis  3. Apply Z transform and DTFT in signal analysis for Discrete time signals.	alysi	s usir		
Further Read	Totals  In Programs using mathematical computing tool for CT and DT system and ZT  In Example 1  After completion of the course, Student will be able to  1. Analyze the properties of signals & systems  2. Apply Laplace transform, Fourier transform in signal analysis  3. Apply Z transform and DTFT in signal analysis for Discrete time si  4. Analyze continuous time LTI systems using Fourier and Laplace Transform and	alysi	s usir		
Further Read  Course Outco	Totals  Ing:  Programs using mathematical computing tool for CT and DT system and ZT  Totals  Programs using mathematical computing tool for CT and DT system and ZT  Totals  After completion of the course, Student will be able to  1. Analyze the properties of signals & systems  2. Apply Laplace transform, Fourier transform in signal analysis  3. Apply Z transform and DTFT in signal analysis for Discrete time signals.	alysi	s usir		
Further Read  Course Outco  References:	Totals  Ing:  Programs using mathematical computing tool for CT and DT system and ZT  Totals  Mes:  After completion of the course, Student will be able to  1. Analyze the properties of signals & systems  2. Apply Laplace transform, Fourier transform in signal analysis  3. Apply Z transform and DTFT in signal analysis for Discrete time si  4. Analyze continuous time LTI systems using Fourier and Laplace Tra  5. Analyze discrete time LTI systems using Z transform.	alysi: gnals	s usir		
Further Read  Course Outco  References:  1. Allan	Programs using mathematical computing tool for CT and DT system and ZT  mes:  After completion of the course, Student will be able to  1. Analyze the properties of signals & systems  2. Apply Laplace transform, Fourier transform in signal analysis  3. Apply Z transform and DTFT in signal analysis for Discrete time si  4. Analyze continuous time LTI systems using Fourier and Laplace Tra  5. Analyze discrete time LTI systems using Z transform.  V.Oppenheim, S.Wilsky and S.H.Nawab, "Signals and Systems", Pearson	gnals ansfo	s usir		
References:  1. Allan 2. B. P. I	Totaling:  Programs using mathematical computing tool for CT and DT system and ZT  mes:  After completion of the course, Student will be able to  1. Analyze the properties of signals & systems  2. Apply Laplace transform, Fourier transform in signal analysis  3. Apply Z transform and DTFT in signal analysis for Discrete time si  4. Analyze continuous time LTI systems using Fourier and Laplace Tra  5. Analyze discrete time LTI systems using Z transform.  V.Oppenheim, S.Wilsky and S.H.Nawab, "Signals and Systems", Pearson athi, "Principles of Linear Systems and Signals", Second Edition, Oxford	gnals ansfo	s usir	ng L'I	
References:  1. Allan 2. B. P. I 3. R.E.Ze	Totaling:  Programs using mathematical computing tool for CT and DT system and ZT  mes:  After completion of the course, Student will be able to  1. Analyze the properties of signals & systems  2. Apply Laplace transform, Fourier transform in signal analysis  3. Apply Z transform and DTFT in signal analysis for Discrete time si  4. Analyze continuous time LTI systems using Fourier and Laplace Tra  5. Analyze discrete time LTI systems using Z transform.  V.Oppenheim, S.Wilsky and S.H.Nawab, "Signals and Systems", Pearson athi, "Principles of Linear Systems and Signals", Second Edition, Oxford timer, W.H.Tranter and R.D.Fannin, "Signals & Systems - Continuous and	gnals ansfo	s usir	ng L'I	
References:  1. Allan  2. B. P. I  3. R.E.Ze Pearso	Totaling:  Programs using mathematical computing tool for CT and DT system and ZT  mes:  After completion of the course, Student will be able to  1. Analyze the properties of signals & systems  2. Apply Laplace transform, Fourier transform in signal analysis  3. Apply Z transform and DTFT in signal analysis for Discrete time si  4. Analyze continuous time LTI systems using Fourier and Laplace Tra  5. Analyze discrete time LTI systems using Z transform.  V.Oppenheim, S.Wilsky and S.H.Nawab, "Signals and Systems", Pearson athi, "Principles of Linear Systems and Signals", Second Edition, Oxford	gnals ansfo	s usir	ng L'I	

1702EC403	ANALOG INTEGRATED CIRCUITS	1 3	T 0	P 0	C 3
Course Obje	ctives:		0		
3	To Learn the fundamental concepts behind transistor biasing and to signal and large signal circuit models	diffe	rentia	te sm	all
	<ol> <li>To Learn the concepts of Analog to digital and Digital to Analog comicroelectronics</li> </ol>	onver	ters fo	r	
	3. To Study the performance metrics of Multistage and Power amplifi				
Unit I	4. To Understand the working of signal generating and wave shaping BASICS OF OPERATIONAL AMPLIFIERS	circu	its	9 1	Hours
	Amplifiers, DC and AC characteristics, Typical op-amp parameters: Finite ga	in fi	nite b		
	ges and currents, Common-mode rejection ratio, Power supply rejection				
	of Op-amp: Precision rectifiers. Summing amplifier, Integrators and diffe				
	fiers. Instrumentation amplifiers, voltage to current converters		,	8	
Unit II	ACTIVE FILTERS			91	Hours
Second order	filter transfer function (low pass, high pass, band pass and band reject), B	utterv	vorth,	Cheb	yshev
and Bessel fil	ters. Switched capacitor filter. notch filter, All pass filters, self-tuned filters				
Unit III	ANALOG TO DIGITAL AND DIGITAL TO ANALOG CONVERTER	RS		91	Hours
	comparator, Schmitt trigger, Astable and monostable multivibrators, Triangu		vave g	genera	itor,
	s using 555 timer, Data converters: A/D and D/A converters				
Unit IV	PHASE LOCKED LOOP				Hours
	block diagram and operation, Four quadrant multipliers. Phase detector,	vco	, App	licatio	ons of
	cy synthesizers, AM detection, FM detection and FSK demodulation				
Unit V	CMOS DIFFERENTIAL AMPLIFIERS				Hours
	and small signal analysis of differential amplifer with Restive load, cu				
	e load, Input common-mode range and Common-mode feedback circuits. O				. Slew
rate, CMRR,	PSRR. Two stage amplifiers, Compensation in amplifiers (Dominant pole con <b>Tot</b> :		sation		Hours
Further Read		aı.		43 1	110415
Turther Read	Collector Emitter Feedback Bias, Bootstrap Darlington Circuit, Effect of En	nitter	or a S	ource	<u> </u>
	Bypass Capacitor on Low frequency response, Comparison of Power Ampli				
	Logic Inverter, CMOS Digital Logic Inverter, BiCMOS Cascade Amplifier,				
	Circuits, CMOS Logic Gate Circuits, Power BJTs, Power MOSFETs.				
Course Outc					
	After completion of the course, Student will be able to				
	<ol> <li>Infer the DC and AC characteristics of operational amplifiers a output and their compensation techniques</li> </ol>	and i	ts effe	ect or	1
	2. Elucidate and design the linear and non linear applications of special application Ics.	an c	pamp	and	
	<ol> <li>Explain and compare the working of multi vibrators using special a general purpose opamp</li> </ol>	pplic	ation l	C 555	5 and
	Classify and comprehend the working principle of data converters				
			1-4	T	or r
	<ol><li>Illustrate the function of application specific ICs such as Volta and its application in communication.</li></ol>	age r	eguiat	ors, i	?LL
References:	and its application in communication.				
	anco, Design with Operational Amplifiers and Analog Integrated Circuits (3/e	?) TM	H. 20	03	
	a and Smith, Microelectronics Circuits, Oxford Univ. Press, 2004	, 1111			
	thlin, Driscoll, OP-AMPS and Linear Integrated Circuits, Prentice Hall, 2001	1.			
4. John	D Ryder, —Electronic fundamentals and Applications: Integrated and Discret		stems	5th	
	on, PHI, 2003 ald .A. Neamen, Electronic Circuit Analysis and Design –2nd edition, Tata M	cGrav	w Hill	. 2009	9
J. Done	as an incurrent, Electronic Circuit indigets and Design 2nd culton, I da wi	Jora	,, 11111	, 200.	

1702EC404	Microprocessors and Microcontrollers	L	T	P	C
1702EC404	(Common to B.E / B.Tech – ECE, CSE & IT)	3	0	0	3
Course Object	ives:				
	1. To understand the architecture and functions of 8085 processor				
	2. To understand the Architecture of 8086 microprocessor				
	3. To understand the concepts of 8051 microcontroller				
	4. To learn the design aspects of I/O and Memory Interfacing circuits.				
	5. To gain the basic knowledge about advanced processors				
Unit I	INTRODUCTION TO MICROPROCESSORS			9 H	ours
	Microprocessors - 8-Bit Processor - 8085 Architecture – Register Organizati m- Addressing Modes – Interrupts- Interrupt Service Routines- Assembly La				
Unit II	THE 8086 MICROPROCESSOR				ours
Introduction to	8086 - Microprocessor architecture - Addressing modes - Instruction set and	d assei	nbler	direct	ives
<ul> <li>Assembly lan</li> </ul>	nguage programming – Modular Programming - Linking and Relocation - S	Stacks	- Pro	cedur	es –
	upts and interrupt service routines - 8086 signals.				
Unit III	MICROCONTROLLER			9 H	ours
	$8051-Special \ Function \ Registers (SFRs)$ - I/O Pins Ports and Circuits - Instr	uction	set		
<ul> <li>Addressing m</li> </ul>	odes - Assembly language programming.				
Unit IV	I/O INTERFACING				ours
	acing and I/O interfacing - Parallel communication interface - Serial comm				
	Interface - Timer - Keyboard /display controller - Interrupt controller				
	nd applications Case studies: Traffic Light control, LED display, LCD disp	lay, K	eyboa	rd dis	play
	larm Controller.				
Unit V	ARCHITECTURE OF ADVANCED PROCESSORS				ours
	configurations - Intel 80286 - Internal Architectural - Register Organiza	.tıon —	Inter	nal B	lock
	chitectural features and Register Organization of i386, i486 and Pentiu				
Diagram – Ar architecture.		ım pro	ocesso	ors. A	RM
architecture.	То		ocesso		RM
	ng:	ım pro	ocesso	ors. A	RM
architecture.  Further Readi	To ng: Intel Core i3, i5 and i7	ım pro	ocesso	ors. A	RM
architecture.	Intel Core i3, i5 and i7 nes:	ım pro	ocesso	ors. A	RM
architecture.  Further Readi	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to	ım pro	ocesso	ors. A	RM
architecture.  Further Readi	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor	ım pro	ocesso	ors. A	RM
architecture.  Further Readi	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor  2. Design and implement the functionality of 8086 microprocessor	ım pro	ocesso	ors. A	RM
architecture.  Further Readi	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor  2. Design and implement the functionality of 8086 microprocessor  3. Design and implement 8051 microcontroller based systems	ım pro	ocesso	ors. A	RM
architecture.  Further Readi	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor  2. Design and implement the functionality of 8086 microprocessor  3. Design and implement 8051 microcontroller based systems  4. Design I/O circuits. Design Memory Interfacing circuits	ım pro	ocesso	ors. A	RM
Further Readi	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor  2. Design and implement the functionality of 8086 microprocessor  3. Design and implement 8051 microcontroller based systems	ım pro	ocesso	ors. A	RM
Further Readi Course Outcoo References:	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor  2. Design and implement the functionality of 8086 microprocessor  3. Design and implement 8051 microcontroller based systems  4. Design I/O circuits. Design Memory Interfacing circuits  5. Acquire the architecture concepts of advanced processors.	tal:	ocesso	45 Ho	ours
Further Readi  Course Outcor  References:  1. Ramesh Ga	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor  2. Design and implement the functionality of 8086 microprocessor  3. Design and implement 8051 microcontroller based systems  4. Design I/O circuits. Design Memory Interfacing circuits  5. Acquire the architecture concepts of advanced processors.	tal:	ocesso	45 Ho	ours
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References:  1. Ramesh Ga Penram Int  2. A. K. Ray and Interfa	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor  2. Design and implement the functionality of 8086 microprocessor  3. Design and implement 8051 microcontroller based systems  4. Design I/O circuits. Design Memory Interfacing circuits  5. Acquire the architecture concepts of advanced processors.  anonkar "Microprocessor Architecture, Programming, and Applications with the ernational Publishing-2000.  & K. M. Bhurchandi, "Advanced Microprocessors and peripherals- Architecture,", TMH, 2002 reprint.	tal:	5"- 5th	n editi	on
References:  1. Ramesh Garand Interfa  2. A. K. Ray and Interfa  3. Mohamed	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor  2. Design and implement the functionality of 8086 microprocessor  3. Design and implement 8051 microcontroller based systems  4. Design I/O circuits. Design Memory Interfacing circuits  5. Acquire the architecture concepts of advanced processors.  anonkar "Microprocessor Architecture, Programming, and Applications with the ternational Publishing-2000.  & K. M. Bhurchandi, "Advanced Microprocessors and peripherals- Architecture," TMH, 2002 reprint.  Ali Mazidi, Janice Gillispie Mazidi, Rolin McKinlay, "The 8051 Microcontrol and the state of the st	tal:	5"- 5th	n editi	on
References:  1. Ramesh Garan Interfa  2. A. K. Rayand Interfa  3. Mohamed Systems: U	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor  2. Design and implement the functionality of 8086 microprocessor  3. Design and implement 8051 microcontroller based systems  4. Design I/O circuits. Design Memory Interfacing circuits  5. Acquire the architecture concepts of advanced processors.  aonkar "Microprocessor Architecture, Programming, and Applications with the ernational Publishing-2000.  & K. M. Bhurchandi, "Advanced Microprocessors and peripherals- Architecting", TMH, 2002 reprint.  Ali Mazidi, Janice Gillispie Mazidi, Rolin McKinlay, "The 8051 Microcontrol (sing Assembly and C", Second Edition, Pearson education, 2011.	ne 808:	5"- 5tl	n editi	on
References:  1. Ramesh Garen Penram Into 2. A. K. Ray and Interfa 3. Mohamed Systems: U. 4. Barry B. BrentiumPro	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor  2. Design and implement 8051 microcontroller based systems  4. Design I/O circuits. Design Memory Interfacing circuits  5. Acquire the architecture concepts of advanced processors.  anonkar "Microprocessor Architecture, Programming, and Applications with the ernational Publishing-2000.  & K. M. Bhurchandi, "Advanced Microprocessors and peripherals- Architecing", TMH, 2002 reprint.  Ali Mazidi, Janice Gillispie Mazidi, Rolin McKinlay, "The 8051 Microcontrol (sing Assembly and C", Second Edition, Pearson education, 2011.  rey, "The Intel Microprocessors, 8086/8088, 80186/80188, 80286, 80386, 80-10 Processor, PentiumII, PentiumIII, PentiumIV, Architecture, Programming & Processor, PentiumIII, PentiumIII, PentiumIV, Architecture, Programming & Processor, PentiumIII, Pe	ne 808:	5"- 5tl	n editi	on
References:  1. Ramesh Garan Interfa  2. A. K. Ray and Interfa  3. Mohamed Systems: U  4. Barry B. B PentiumPre Edition, Pe	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor  2. Design and implement the functionality of 8086 microprocessor  3. Design and implement 8051 microcontroller based systems  4. Design I/O circuits. Design Memory Interfacing circuits  5. Acquire the architecture concepts of advanced processors.  annkar "Microprocessor Architecture, Programming, and Applications with the ernational Publishing-2000.  & K. M. Bhurchandi, "Advanced Microprocessors and peripherals- Architecting", TMH, 2002 reprint.  Ali Mazidi, Janice Gillispie Mazidi, Rolin McKinlay, "The 8051 Microcontrol (sing Assembly and C", Second Edition, Pearson education, 2011.  rey, "The Intel Microprocessors, 8086/8088, 80186/80188, 80286, 80386, 8040 Processor, PentiumII, PentiumIII, PentiumIV, Architecture, Programming & arson Education/PHI, 2002.	ne 808:	5"- 5tl	n editi	on
References:  1. Ramesh Garen Interfa  2. A. K. Ray and Interfa  3. Mohamed Systems: U  4. Barry B. BrentiumPro	Intel Core i3, i5 and i7  mes:  After completion of the course, Student will be able to  1. Design and implement the functionality of 8085 microprocessor  2. Design and implement the functionality of 8086 microprocessor  3. Design and implement 8051 microcontroller based systems  4. Design I/O circuits. Design Memory Interfacing circuits  5. Acquire the architecture concepts of advanced processors.  anonkar "Microprocessor Architecture, Programming, and Applications with the ernational Publishing-2000.  & K. M. Bhurchandi, "Advanced Microprocessors and peripherals- Architecing", TMH, 2002 reprint.  Ali Mazidi, Janice Gillispie Mazidi, Rolin McKinlay, "The 8051 Microcontrol (sing Assembly and C", Second Edition, Pearson education, 2011.  rey, "The Intel Microprocessors, 8086/8088, 80186/80188, 80286, 80386, 8040 Processor, PentiumII, PentiumIII, PentiumIV, Architecture, Programming & arson Education/PHI, 2002.  w.intel.in	ne 808:	5"- 5tl	n editi	on

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1702EC405	TRANSMISSION LINES AND WAVEGUIDES	1 3	T 0	P 0	<u>C</u>
Course Object	tives:				
	1. To introduce the various types of transmission lines and to discuss the los	ses ass	ociat	ed.	
	2. To give thorough understanding about impedance transformation and mat	ching.			
	3. To use the Smith chart in problem solving.				
	4. To impart knowledge on filter theories and waveguide theories.				
	5. To introduce the various types of transmission lines and to discuss the los	ses ass	ociat	ed.	
Unit I	TRANSMISSION LINE THEORY			9 H	
	on of transmission line – The two standard forms for voltage and current				
	nce - Physical significance of the equation and the infinite line - Refl				
	nd velocity of propagation – Waveform distortion – Distortion less trans				
	le – Inductance loading of telephone cables – Input impedance of lossles		s – R	leflec	tion
	erminated by Z <sub>O</sub> – Transfer impedance – Reflection factor and reflection lo	oss.			
Unit II	IMPEDANCE MATCHING IN TRANSMISSION LINES			9 H	
_	ves and standing wave ratio on a line – One eighth wave line – Quan				
	natching – The half-wave line –Smith chart – Application of the smith of				
	nce to reflection co-efficient and vice-versa – Impedance to adn				
and vice-vers	sa – Input impedance of a lossless line terminated by an impedance – Single matching	ngie si	uo II	iatem	ng
Unit III	FILTERS AND GUIDED WAVES			9 H	ours
		rod so	otion		
	Filters - Low pass, High pass band, pass band elimination filters - m -derivable planes of perfect conductors – Transverse electric and transverse				
	cs of TE and TM waves – Transverse electromagnetic waves – Velocities				
	uniform plane waves between parallel planes – Attenuation of TE and TN				
	-Wave impedances.	·i ·······	<b>C</b> 5 III	puru	1101
Unit IV					
CILLIA	RECTANGULAR WAVEGUIDES			9 Ho	ours
	RECTANGULAR WAVEGUIDES  nagnetic waves in rectangular wave guides – Transverse electric wa	ves in	rec		
Transverse r	nagnetic waves in rectangular wave guides – Transverse electric wa			tangu	lar
Transverse r waveguides	nagnetic waves in rectangular wave guides – Transverse electric wave – Characteristics of TE and TM waves – Cutoff wavelength and	phase	vel	tangu ocity	lar –
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Transverse r waveguides Impossibility	magnetic waves in rectangular wave guides — Transverse electric wave. — Characteristics of TE and TM waves — Cutoff wavelength and of TEM waves in waveguides — Dominant mode in rectangular waveguides.	phase de – A	vel ttenu	tangu ocity ation	lar – of
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6. B.Somanathan Nair, Transmission Lines and Wave guides, Sanguine Technical publishers, 2006.

Course Objectiv	In this contro     To the invaria     In dea and tir	course it is aimed to introduce to the students the principles a l systems.  basic concepts of block diagram reduction, time domain analyant systems.  ls with the different aspects of stability analysis of systems in me domain.	ysis so	olutio		
Course Objectiv	In this contro     To the invaria     In dea and tir	l systems.  basic concepts of block diagram reduction, time domain analyant systems.  Is with the different aspects of stability analysis of systems in	ysis so	olutio		
_	invaria 3. In dea and tir	ant systems.  Is with the different aspects of stability analysis of systems in			ns to t	
	and tir	* * * *	frague			
<u> </u>	4 To una		neque	ency c	lomai	n ——
		derstand the application of control system.				
		course it is aimed to introduce to the students the principles a	nd ap	olicati	ons o	f
		l systems.				
		TION OF CONTROL SYSTEMS			9 H	
Block diagram	algebra - Rep	tems - Open loop and closed loop control systems and presentation by signal flow graph - Reduction using Mas effect of feedback				
		ONSE ANALYSIS			9 H	ours
		ime response of first order system - Transient respons	se of	seco		
	domain specit	fication - steady state response - Steady state error - Ef				
Unit III 1	FREQUENCY	Y RESPONSE ANALYSIS			9 H	ours
Frequency response	onse - Frequ	ency domain specification - stability analysis from bode	plot	, po	lar pl	ot ,
nyquist plot - C	Compensation	techniques - Lag , Lead , lead-lag controllers design in f	reque	ncy d	omaiı	1.
Unit IV S	STABILITY A	ANALSIS AND ROOT LOCUS TECHNIQUES			9 H	ours
Concept of stab		Hurwitz criterion - Nyquist stability criterion - Routh lo	cus c	oncep	t -	
		ONS OF CONTROL SYSTEMS			9 H	ours
Aircraft flight of	control system	ns - Director(military) - Embedded instrumentation - Finantrol - Laser ignition - Weight shift control				
		Tot	al:		45 H	ours
Further Reading	g:					
I	Modern contro	l systems.				
Course Outcome	es:					
1		on of the course, Student will be able to				
	1. Knowledge control sy	e on open loop and closed loop control system, conce	ept o	f fee	dback	in
		function representation through block diagram algebra and	l sign	al flov	v gra	ph,
		onse analysis.				-
		response analysis through bode plot, polar plot, nyquist	plot	and b	oasics	of
	state spac	e analysis.				
References:						
2. Control and	ontrol systems, Dynamical Sy N UNIVERSIT	, third edition, Benjamin C. Kuo. stems, Karl Johan Aström <sup>°</sup> Richard M. Murray, Version v2. TY PRESS	10c (N	/Iarch	4, 20	10),
3. Modern Con		TWELFTH EDITION, Richard C. Dorf University of Californ	ia, Da	vis, R	lobert	H.

1702EC451	ANALOG INTEGRATED CIRCUITS LABORATORY	L 0	T 0	P 3	<u>C</u>
Course Object	tives:		U		
· ·	To expose the students to linear and integrated circuits				
	2. To understand the basics of linear integrated circuits and available ICs				
	3. To understand characteristics of operational amplifier				
	4. To apply operational amplifiers in linear and nonlinear applications.				
	5. To acquire the basic knowledge of special function IC				
	6. To use PSPICE software for circuit design				
List of Experi	ments:				
1. Inverting,	Non inverting and Differential amplifiers.				
	and Differentiator.				
3. Instrument	ation Amplifier				
4. Active low	-pass, High-pass and band-pass filters.				
5. Astable &	Monostable multivibrators and Schmitt Trigger using op-amp				
6. Phase shift	and Wien bridge oscillators using op-amp.				
7. Astable an	d monostable multivibrators using NE555 Timer				
8. PLL charac	cteristics and its use as Frequency Multiplier				
9. DC power	supply using LM317 and LM723				
10. Mini proje	ct using Op-Amp and Specialized IC's				
SIMULATIO	N USING SPICE				
11. Analog mu	ıltiplier				
12. CMOS Inv	verter, NAND and NOR				
		Tota	al:	45 H	urs
Additional Ex	periments:				
	Buck-Boost Converter				
	2. Design a circuit for Lisajious Figure				
Course Outco					
	After completion of the course, Student will be able to				
	Design oscillators and amplifiers using operational amplifiers				
	2. Design filters using Opamp and perform experiment on frequency respo	nse			
	3. Analyse the working of PLL and use PLL as frequency multiplier				
	4. Design DC power supply using ICs				
	5. Analyse the performance of oscillators and multivibrators using SPICE				
References:					
1. Adel.	S. Sedra, Kenneth C. Smith, Microelectronic Circuits Theory an Application	ns ,5	th Ed	lition,	
Oxfor	d University, 2006.				
2. Jacob	Millman, C. Halkias and Satyabrata Jit, Electronic Devices and Circuits, 3	rd Ed	ition,	Tata	

 Jacob Millman, C. Halkias and Satyabrata Jit, Electronic Devices and Circuits, 3rd Edition, Tata McGraw-Hill, 2011.

1702EC452	Microprocessors and Microcontrollers Laboratory	L	T 0	P 4	C
Course Object	ives: (Common to B.E / B.Tech – ECE,CSE & IT)	0	U	4	2
Course Object	1. Write ALP for arithmetic and logical operations in 8085, 8086 and 8051				
	Write ALT for artifilited and logical operations in 8063, 8060 and 8051     Differentiate Serial and Parallel Interface				
	Interface different I/Os with Microprocessors& Microcontrollers				
	Be familiar with MASM				
List of Experi					
8085 Program					
	metic and Logical operations				
	Searching the given data.				
	s using kits with MASM				
3. Floating po	int operations				
8051 Experime	ents using kits				
4. Basic arith	metic and Logical operations				
5. Square and	Find 2's complement of a number				
6. Code conv	ersion				
Peripherals an	d Interfacing Experiments				
7. Traffic ligh					
	tor and DC Motor control				
9. Key board					
	face and Parallel interface				
11. Printer Inte					
12. A/D and D	/A interface and Waveform Generation				
		Tota	al:	45 H	ours
Additional Ex					
	Basic experiments using Arduino processor				
Course Outcon					
	After completion of the course, Student will be able to				
	1. Write ALP Programmes for fixed and Floating Point and Arithmetic				
	2. Interface different I/Os with processor				
	3. Generate waveforms using Microprocessors&Execute Programs in 8051				
	4. Explain the difference between simulator and Emulator				
References:	4.5.46.4.86				
	1. Ramesh Gaonkar "Microprocessor Architecture, Programming, and Appl	icatio	ons w	ith the	:
	8085"- 5th edition Penram International Publishing-2000.				
	2. A. K. Ray & K. M. Bhurchandi, "Advanced Microprocessors and periph	erals	- Arc	hitecti	ıres,
	Programming and Interfacing", TMH, 2002 reprint.				

1704GE451	LIFE SKILLS: VERBAL ABILITY	L 0	T 0	P 2	C
Course Object	tives:		U		
	1. To help students comprehend and use vocabulary words in communication.	their	day	to	day
	2. To apply appropriate reading strategies for interpreting technical documents used in job-related settings.				
	3. To ensure students will be able to use targeted grammatical structure appropriately in oral and written production.				
	4. To enable the students to arrange the sentences in meaningful un whether constructions rely on active or passive voice				nine
TT 1/ T	5. To Apply the principles of effective business writing to hone commu	ınıcat	ion sk		
Unit I Introduction -	VOCABULARY USAGE Synonyms and Antonyms based on Technical terms – Single word Substite listening activity.	tution	– Ne		ours aper,
Unit II	COMPREHENSION ABILITY			9 H	ours
	Scanning – Social Science passages – Business and Economics passages –	– late	st pol		
current event b	pased passages – Theme detection – Deriving conclusion from passages	iuic	ot por	iticai	una
Unit III	BASIC GRAMMAR AND ERROR DETECTION			9 H	ours
Parallelism –	Redundancy - Ambiguity - Concord - Common Errors - Spotting	Erro	rs –	Sent	ence
	- Error Detection FAQ in Competitive exams.				
Unit IV	REARRANGEMENT AND GENERAL USAGE			9 H	ours
Jumble Senter	ices – Cloze Test - Idioms and Phrases – Active and passive voice – Spellir	ng tes	t.		
Unit V	APPLICATION OF VERBAL ABILITY			9 H	ours
	ing - Business Vocabulary - Delivering Good / Bad News - Media Com		cation	1 - E	mail
Etiquette – Re	port Writing - Proposal writing - Essay writing - Indexing - Market surveying				
	Tot	al:		45 H	ours
Further Read					
G 0 1	Modern control systems.				
Course Outco					
	After completion of the course, Student will be able to		•		
	1. Students are enabled to use new words in their day to day commu				
	<ul><li>2. Students are capable to gather information swiftly while reading</li><li>3. Students are proficient during their oral and written communicati</li></ul>		ges		
			ha wai	ico o:	f tha
	4. Students are equipped to rearrange the sentences and able to idensentence	itiiy t	ne vo	ice o.	i tile
	5. Students use their knowledge of the best practices to craft documents	effe	ctive	busi	ness
References:					
	Sharma and Meenakshi Upadhyav, How to Prepare for Verbal Al rehension for CAT, McGrawHill Publication, Seventh Edition 2017	oility	and	Rea	ding
2. R S	Aggarwal and Vikas Aggarwal, Quick Learning Objective General thing House, 2017	Eng	lish	,S.Cl	nand
3. Dr.K.	Alex, Soft Skills, S.Chand Publishing House, Third Revise Edition, 2014				