E.G.S. PILLAY ENGINEERING COLLEGE

(Autonomous)

Approved by AICTE, New Delhi | Affiliated to Anna University, Chennai Accredited by NAAC with 'A'Grade | Accredited by NBA

NAGAPATTINAM – 611002



B.TECH. INFORMATION TECHNOLOGY R-2019

| | SEMESTE | R III | | | | | | | |
|---------------------|---|-------|---|---|----|-----|------|-------|----------|
| Course Code | Course Name | L | Т | P | С | Max | imum | Marks | Cotogowy |
| Course Code | Course Name | L | 1 | r | ١ | CA | ES | Total | Category |
| Theory Course | e | | | | | | | | |
| 1901MA302 | Engineering Mathematics III (Queuing Model and Network Model) | 3 | 2 | 0 | 4 | 40 | 60 | 100 | BS |
| 1902IT301 | Data Structures and Algorithms | 3 | 0 | 0 | 3 | 40 | 60 | 100 | PC |
| 1902IT302 | Computer Organization and Architecture | 3 | 0 | 0 | 3 | 40 | 60 | 100 | PC |
| 1902IT303 | Digital Principles and Design | 3 | 0 | 0 | 3 | 40 | 60 | 100 | PC |
| 1902IT304 | Problem solving using Python | 3 | 0 | 0 | 3 | 40 | 60 | 100 | PC |
| 1901GEX04 | Biology for Engineers | 3 | 0 | 0 | 3 | 40 | 60 | 100 | BS |
| Laboratory C | ourse | | | | | | | | |
| 1902IT351 | Data Structures and Algorithms Lab | 0 | 0 | 2 | 1 | 50 | 50 | 100 | PC |
| 1902IT352 | Digital Principles and Design Lab | 0 | 0 | 2 | 1 | 50 | 50 | 100 | PC |
| 1902IT353 | Python Programming Lab | 0 | 0 | 2 | 1 | 50 | 50 | 100 | PC |
| 1904GE351 | Life Skills: Verbal Ability | 0 | 0 | 2 | 1 | 100 | - | 100 | EEC |
| Audit Course | | | | | | | | | |
| 1902MCX02 | Constitution of India | 2 | 0 | 0 | 0 | 100 | - | 100 | |
| | Total | 20 | 2 | 8 | 23 | 590 | 510 | 1100 | - |

| 1901MA302 | | ENGINEERING MATHEMATICS III | L | T | P | C |
|--|--|--|---|--|--------------------------------------|--------|
| | | (Queuing Model and Network Model) | 3 | 2 | 0 | 4 |
| PREREQUIS | | | | | | |
| | | eering Mathematics I | | | | |
| | 2. Engine | eering Mathematics II | | | | |
| COURSE OBJ | | | | | | |
| | | duce Fourier series analysis and applications in Engineering, apart | t from | ı its u | ise in | |
| | | andary value problems. | | | | |
| | | aint the student with Fourier transform techniques used in wide va | | | | |
| | | duce the effective mathematical tools for the solutions of partial di | | | | |
| | | several physical processes and to develop Z transform techniques | s for d | iscre | te tim | e |
| | systems. | | | | | |
| UNIT I | FOURIER | SEDIES | | | 12 H | 011100 |
| | | eneral Fourier series – Odd and even functions – Half range sine | ceries | | | |
| | | identity – Harmonic analysis – Simple Applications | SCITCS | , — II | an rai | igc |
| UNIT II | | TRANSFORMS | | | 12 H | niirc |
| | | gral theorem – Fourier transform pair – Fourier sine and cosine tra | ansfor | | | Juis |
| | | of simple functions – Convolution theorem – Parseval's identity | 4110101 | 1115 | | |
| UNIT III | | IG MODELS | | | 12 H | ours |
| | | ng Models – Markovian Queues – (M / M / 1): (FIFO / /),(M / | / M / | | | |
| | | /),(M / M / C): (FIFO / N /)models – Little"s formulae. | | -) (- | | |
| , , , | , , | K MODEL | | | 12 H | |
| | | | | | | ours |
| | struction – | Critical Path Method – Project Evaluation and Review Technique | que – | - Res | ource | |
| Network Con | | Critical Path Method – Project Evaluation and Review Technic eduling. | que – | Res | ource | |
| | etwork Sch | · · | que – | | ource | |
| Network Con analysis in No UNIT V | etwork Sch TRANSPO | eduling. ORTATION AND ASSIGNMENT MODELS | | | 12 H | |
| Network Con analysis in No UNIT V Mathematical | etwork Sch TRANSPO formulation | eduling. | ible so | olutio | 12 H on – | ours |
| Network Con analysis in No UNIT V Mathematical | TRANSPO formulation tion - degen | eduling. DRTATION AND ASSIGNMENT MODELS n of transportation problem- Methods for finding initial basic feasi eracy – Mathematical formulation of assignment models – Hunga nt problem | ible so | olutio Algor | 12 H on – rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solu Variants of the | etwork Sch TRANSPO formulation tion - degen e Assignme | eduling. DRTATION AND ASSIGNMENT MODELS n of transportation problem- Methods for finding initial basic feasi eracy – Mathematical formulation of assignment models – Hunga nt problem TOTAL | ible so | olutio Algor | 12 H on – | ours |
| Network Con analysis in No UNIT V Mathematical optimum solu Variants of the | TRANSPO formulation tion - degen e Assignme | eduling. DRTATION AND ASSIGNMENT MODELS of transportation problem- Methods for finding initial basic feasieracy – Mathematical formulation of assignment models – Hungant problem TOTAL SEMINAR: | ible so | olutio Algor | 12 H on – rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solu Variants of the | TRANSPO formulation tion - degen e Assignme READING | eduling. DRTATION AND ASSIGNMENT MODELS of transportation problem- Methods for finding initial basic feasi eracy – Mathematical formulation of assignment models – Hungant problem TOTAL SEMINAR: | ible so | olutio Algor | 12 H on – rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solu Variants of the | TRANSPO formulation tion - degen e Assignme READING / 1. Linear 2. Solution | PARTATION AND ASSIGNMENT MODELS of transportation problem- Methods for finding initial basic feasi eracy – Mathematical formulation of assignment models – Hungant problem TOTAL SEMINAR: partial differential equations of higher order on of non-homogeneous partial differential equations | ible so | olutio Algor | 12 H on – rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solu Variants of the | tion - degene Assignme READING 1. Linear 2. Solution TRANSPO | eduling. DRTATION AND ASSIGNMENT MODELS n of transportation problem- Methods for finding initial basic feasi eracy – Mathematical formulation of assignment models – Hunga nt problem TOTAL SEMINAR: partial differential equations of higher order on of non-homogeneous partial differential equations : | ible so | olutio Algor | 12 H on – rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solu Variants of the FURTHER F | rranspo formulation tion - degen e Assignme READING 1. Linear 2. Solution UTCOMES After comp | eduling. DRTATION AND ASSIGNMENT MODELS of transportation problem- Methods for finding initial basic feasieracy – Mathematical formulation of assignment models – Hungant problem TOTAL SEMINAR: partial differential equations of higher order on of non-homogeneous partial differential equations cletion of the course, Student will be able to | ible so | olutio Algor | 12 H on – rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solu Variants of the FURTHER F COURSE OU | TRANSPO formulation - degen e Assignme READING / 1. Linear 2. Solutio UTCOMES After comp Compute the | eduling. DRTATION AND ASSIGNMENT MODELS of transportation problem- Methods for finding initial basic feasieracy – Mathematical formulation of assignment models – Hungant problem TOTAL SEMINAR: partial differential equations of higher order on of non-homogeneous partial differential equations cluster of the course, Student will be able to the solution of partial differential equations | ible so arian / | olutio Algor | 12 H on – rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solu Variants of the FURTHER F COURSE OU CO1 CO2 | rranspo formulation tion - degen e Assignme READING / 1. Linear 2. Solutio UTCOMES After comp Compute the | PATATION AND ASSIGNMENT MODELS In of transportation problem- Methods for finding initial basic feasi eracy – Mathematical formulation of assignment models – Hunga not problem TOTAL SEMINAR: In partial differential equations of higher order In on of non-homogeneous partial differential equations In partial differential equations | ible so arian / | olutio Algor | 12 H on – rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solur Variants of the FURTHER F COURSE OU CO1 CO2 CO3 | rranspo formulation tion - degen e Assignme READING 1. Linear 2. Solution TCOMES After comp Compute the Use Fourie Solve various | eduling. DRTATION AND ASSIGNMENT MODELS In of transportation problem- Methods for finding initial basic feasi eracy — Mathematical formulation of assignment models — Hunga ant problem TOTAL SEMINAR: In partial differential equations of higher order on of non-homogeneous partial differential equations in pletion of the course, Student will be able to the solution of partial differential equations on series analysis which is central to many applications in engineer ous Queueing model problems | ible so arian / | olutio Algor | 12 H on – rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solu Variants of the FURTHER F COURSE OU CO1 CO2 CO3 CO4 | rranspo formulation tion - degen e Assignme READING 1. Linear 2. Solution TCOMES After comp Compute the Use Fourie Solve varion Apply Netve | eduling. DRTATION AND ASSIGNMENT MODELS of transportation problem- Methods for finding initial basic feasieracy — Mathematical formulation of assignment models — Hungant problem TOTAL SEMINAR: partial differential equations of higher order on of non-homogeneous partial differential equations cletion of the course, Student will be able to ne solution of partial differential equations r series analysis which is central to many applications in engineer ous Queueing model problems work model used in wide variety of situations | ible so arian / | olutio Algor | 12 H on – rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solu Variants of the FURTHER F COURSE OU CO1 CO2 CO3 CO4 CO5 | rranspo formulation tion - degen e Assignme Linear 2. Solutio TCOMES After comp Compute th Use Fourie Solve vario Apply Netw Apply Transport | eduling. DRTATION AND ASSIGNMENT MODELS In of transportation problem- Methods for finding initial basic feasi eracy — Mathematical formulation of assignment models — Hunga ant problem TOTAL SEMINAR: In partial differential equations of higher order on of non-homogeneous partial differential equations in pletion of the course, Student will be able to the solution of partial differential equations on series analysis which is central to many applications in engineer ous Queueing model problems | ible so arian / | olutio Algor | 12 H on – rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solu Variants of the FURTHER F COURSE OU CO1 CO2 CO3 CO4 CO5 REFERENC | rranspo formulation - degen e Assignme READING 1. Linear 2. Solution Tromes After comp Compute the Use Fourie Solve varion Apply Netw Apply Transport ES: | PATATION AND ASSIGNMENT MODELS of transportation problem- Methods for finding initial basic feasieracy – Mathematical formulation of assignment models – Hungant problem TOTAL SEMINAR: partial differential equations of higher order on of non-homogeneous partial differential equations cluster of the course, Student will be able to the solution of partial differential equations reseries analysis which is central to many applications in engineer ous Queueing model problems work model used in wide variety of situations asportation and Assignment techniques for discrete time systems. | ible so arian A | olutio | 12 Hon — rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solur Variants of the Variants of the COURSE OU CO1 CO2 CO3 CO4 CO5 REFERENC 1. Veeraraja | rransport Sch TRANSPO formulation - degen e Assignme READING 1. Linear 2. Solution Tromes After comp Compute the Use Fourie Solve varion Apply Netw Apply Transport Es: In. T., "Transport Transport Transpor | PRTATION AND ASSIGNMENT MODELS of transportation problem- Methods for finding initial basic feasi eracy – Mathematical formulation of assignment models – Hungant problem TOTAL SEMINAR: partial differential equations of higher order on of non-homogeneous partial differential equations cluster of the course, Student will be able to the solution of partial differential equations reseries analysis which is central to many applications in engineer ous Queueing model problems work model used in wide variety of situations asportation and Assignment techniques for discrete time systems. In the state of the state of the system | ible so arian A | olutio | 12 Hon — rithm | ours |
| Network Con analysis in No UNIT V Mathematical optimum solur Variants of the FURTHER FOR COURSE OU CO1 CO2 CO3 CO4 CO5 REFERENC 1. Veeraraja Education | rransport formulation tion - degen e Assignme READING 1. Linear 2. Solution TCOMES After comp Compute the Use Fourie Solve varion Apply Netwon Apply Transport ES: In T., "Transport of the Pvt. Ltd., it | PRTATION AND ASSIGNMENT MODELS In of transportation problem- Methods for finding initial basic feasiveracy — Mathematical formulation of assignment models — Hunga ant problem TOTAL SEMINAR: In partial differential equations of higher order In on of non-homogeneous partial differential equations In partial differential equations In partial differential equations In series analysis which is central to many applications in engineer ous Queueing model problems Work model used in wide variety of situations In sportation and Assignment techniques for discrete time systems. In partial Differential Equations Second reprint, Tata Mew Delhi, 2012 | ible so arian A | olutio | 12 Hon — rithm O HOU | ours |
| Network Conanalysis in Notanalysis in Notanalysi in Notanalysi in Notanalysi in Notanalysi in Notanalysi in Not | TRANSPO formulation tion - degen e Assignme READING 1. Linear 2. Solution Tromes After comp Compute the Use Fourier Solve varion Apply Netwo | PATATION AND ASSIGNMENT MODELS of transportation problem- Methods for finding initial basic feasieracy — Mathematical formulation of assignment models — Hungant problem TOTAL SEMINAR: partial differential equations of higher order on of non-homogeneous partial differential equations cletion of the course, Student will be able to ne solution of partial differential equations reseries analysis which is central to many applications in engineer ous Queueing model problems work model used in wide variety of situations asportation and Assignment techniques for discrete time systems. In partial Differential Equations storms and Partial Differential Equations reserved to many applications in engineer ous Queueing model problems work model used in wide variety of situations asportation and Assignment techniques for discrete time systems. In partial Differential Equations storms and Partial Differential Equations Total Total | ible so arian / | olution of the control of the contro | 12 Hoon — rithm O HOO | URS |
| Network Conanalysis in No UNIT V Mathematical optimum solu Variants of the FURTHER F COURSE OU CO1 CO2 CO3 CO4 CO5 REFERENC 1. Veeraraja Education 2. Grewal. E 3. Bali.N.P.a | rranspo formulation - degen e Assignme READING / 1. Linear 2. Solutio TCOMES After comp Compute th Use Fourie Solve vario Apply Netw Apply Tran ES: In. T., "Tran i Pvt. Ltd., i B.S., "Highe and Manish | PRTATION AND ASSIGNMENT MODELS In of transportation problem- Methods for finding initial basic feasiveracy — Mathematical formulation of assignment models — Hunga ant problem TOTAL SEMINAR: In partial differential equations of higher order In on of non-homogeneous partial differential equations In partial differential equations In partial differential equations In series analysis which is central to many applications in engineer ous Queueing model problems Work model used in wide variety of situations In sportation and Assignment techniques for discrete time systems. In partial Differential Equations Second reprint, Tata Mew Delhi, 2012 | ible so arian / | olution of the control of the contro | 12 Hoon — rithm O HOO | URS |
| Network Conanalysis in Notanalysis i | rransport Sch. TRANSPO formulation - degen e Assignme READING A 1. Linear 2. Solution TCOMES After compute the Use Fourier Solve varion Apply Netwon Apply Transport Scheme Sch | PATATION AND ASSIGNMENT MODELS of transportation problem- Methods for finding initial basic feasieracy — Mathematical formulation of assignment models — Hungant problem TOTAL SEMINAR: partial differential equations of higher order on of non-homogeneous partial differential equations cletion of the course, Student will be able to ne solution of partial differential equations reseries analysis which is central to many applications in engineer ous Queueing model problems work model used in wide variety of situations asportation and Assignment techniques for discrete time systems. In partial Differential Equations storms and Partial Differential Equations reserved to many applications in engineer ous Queueing model problems work model used in wide variety of situations asportation and Assignment techniques for discrete time systems. In partial Differential Equations storms and Partial Differential Equations Total Total | ible so arian A | olution olutitai olution olution olution olution olution olution olution oluti | 12 Hon — rithm O HO | URS |
| Network Conanalysis in Notanalysis i | rransport formulation - degeneration | PRTATION AND ASSIGNMENT MODELS In of transportation problem- Methods for finding initial basic feasiveracy – Mathematical formulation of assignment models – Hungant problem TOTAL SEMINAR: Partial differential equations of higher order on of non-homogeneous partial differential equations: Determined by the course of the course of the solution of partial differential equations of the sol | ible so arian // L: ring McGra Delhi, Laxmi | olution Algorian Algo | 12 Hon — rithm D HOI fill lication | URS |
| Network Conanalysis in Notanalysis i | rransportion - degeneration - degene | PRTATION AND ASSIGNMENT MODELS In of transportation problem- Methods for finding initial basic feasi eracy – Mathematical formulation of assignment models – Hunga int problem TOTAL SEMINAR: In partial differential equations of higher order In on of non-homogeneous partial differential equations In partial differential equations In partial differential equations In partial differential equations In series analysis which is central to many applications in engineer ous Queueing model problems In work model used in wide variety of situations In sportation and Assignment techniques for discrete time systems. In partial Differential Equations, Second reprint, Tata Mayor Delhi, 2012 In Engineering Mathematics, 42nd Edition, Khanna Publishers, D. Goyal, "A Textbook of Engineering Mathematics, 7th Edition, In the Engineering Mathematics, Tata Mayor Pillay. T.K. The Engineering Mathematics, Tata Mc-GrawHill Publishing Comercial and Policy and Policy Comercial Equations, Tata Mc-GrawHill Publishing | ible so arian // L: ring McGra Delhi, Laxmi | olution Algorian Algo | 12 Hon — rithm D HOI fill lication | URS |
| Network Conanalysis in Notanalysis i | rransport formulation tion - degen e Assignme READING 1. Linear 2. Solution Tromport Compute the Use Fourier Solve variete Apply Netwon Apply Transport ES: In. T., "Transport Pvt. Ltd., N B.S., "Higher and Manish 2007 B.V., "Higher O8. an.S., Maniete "Vol. II & 2. | PRTATION AND ASSIGNMENT MODELS In of transportation problem- Methods for finding initial basic feasiveracy – Mathematical formulation of assignment models – Hungant problem TOTAL SEMINAR: Partial differential equations of higher order on of non-homogeneous partial differential equations: Determined by the course of the course of the solution of partial differential equations of the sol | ible so arian // L: ring McGra Delhi, Laxmi | olution Algorian Algo | 12 Hon — rithm D HOI fill lication | URS |

B.Tech. Information Technology | E.G.S. Pillay Engineering College (Autonomous) Regulations 2019 |

Approved in IV Academic Council Meeting held on 25-05-2019

1902IT301 | DATA STRUCTURES AND ALGORITHMS | L | T | P | C

| 190211301 | | | | | | | | | D. | A | Ľ. | 1 | L E | A | ١ | , | 3 | • | 1 | Ĺ | ŀ | < | U | J | C | ,] | U | U | 1 | • | ť | 'n | • | 1 | • | 1 | N | L |) | 1 | 4 | ١ | J | L | _ | 1 | l | J | J | • | l | ر |) | l | K | J | ľ | L | 1 | .1 | N | /] | ì | • | | _ | | | | | L | 1 | ╧ | Ί | | Ц | | ľ | | | | l |) | |
|----------------|----------------|--------|------|------|------------|----------|-----|----|----|----------|------------|----|-----|-----|-----------|------------|----|----|----|----|----|----|--------|----|-----------|-----|-----|------------|-----|------------|---|----|-----|------------|----|----------|----|---------|----|----|---|----|-----|----|------------|---|----|----|----|----|-----|----|----|-----|----|-----|-----|----|----|----|----|----|----|----|----|----|--|---|-----|---------|----------|-----------------------|-----|-----|----|----------|-----|---------|-----|----|------|----|----|----------|
| | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | 3 | | | 0 | | | (| 0 | | | | 3 | ţ | |
| Aim: This co | ourse is used | l to s | stı | stı | tı | tu | u | 10 | dy | y | a | 11 | n | d | 1 | ι | u | ır | n | ıC | 10 | e | r | S | ta | ın | d | | tl | 16 | • | C | c |)1 | 1 | С | e | p | t | S | | C |) | f | f |] | Ι | С |) |); | a | ı | t | 2 | 1 | | 5 | tı | 1 | l | 21 | U | 11 | • | 25 | 3 | a | n | d | A | 18 | 30 | r | th | n | 18 | 3 | | | | | | | |
| PREREQUI | | Pro | rog | gı | gı | gr | ζľ | ra | ın | 'n | n | n | iı | n | ٤ | 3 | i | iı | n | ì | (| 2 | | a | n | d | C | <u>'</u> - | | + | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | | | | | | | | | | | | | | | |
| COURSE OBJ | | | | | | | | | _ | | _ | | _ | _ | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | _ | _ | | _ | | | | | | | | | | | |
| | | arn 1 | | | | | | | | | | _ | | | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | | | | _ | - | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | - | _ | _ | _ | _ | _ | | | | | | | | | | | _ | | _ | _ | | _ | | | | | | | | | | | |
| | | ıdy 1 | | | | | | | | | | | | | | | | | | _ | _ | _ | _ | _ | | | | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | - | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | - | _ | _ | _ | _ | _ | _ | | | | | | | | | | _ | | _ | _ | | _ | | | | | | | | | | | |
| | | e va | | | | | | | | | C | d | a | ιt | a | ì | S | S1 | t: | r | υ | 10 | C | tı | ır | e | S | 8 | ľ | 10 | l | a | 1 | g | C |)1 | i | tl | h | n | n | S | S | , | t | t | e |) | (| С | ŀ | h | 1 | n | ıi | i | ľ | u | e | S | 1 | Ċ |)1 | • | r | e | a | 1 | tiı | n | e | e | X | ım | p | 16 | | | | | | | | |
| UNIT I | INTRODU | | | | | | | | | | _ | | _ | _ | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | _ | _ | | _ | | | | | | H | | | ır | .5 | ; |
| Data Structu | | | | | | | | | | | | | | | | | | | | | | | | | | 4 | D |) | Γ | - | - | 4 | 4 | .1 | ٤ | (|)] | ri | t | h | 1 | 1 | n | 15 | S | , | | - | | - | | | F | • | r | O | ł |) | l | 21 | n | | S | (|) | ١ | ⁄i | n | g | _ | (| \mathbb{C}^{α} |)1 | np | 16 | 23 | ۲i۱ | ty | · – | - | | | | |
| Asymptotic | | | | _ | | | | | | | _ | _ | _ | _ | _ | _ | ŀ | la | a | ιt | i | C |)1 | 18 | S | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | | | _ | | _ | | _ | _ | | _ | | _ | _ | | | | | | | _ | |
| UNIT II | DATA ST | | | | | | | | | | | | | | | | _ | _ | _ | _ | _ | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | | | _ | _ | | | | | | | | L | | | | H | | | ır | .5 | ; |
| Array – List | • • | • | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | - | | | | | | | | | | | | | | | | | | | - | | • | | | | | | | | | | | | • | | | | | | | 01 | ns | . – | - | | | | |
| Queue: Oper | | | | | | | | | | | | | | - | _ | | | | | _ | _ | _ | _ | _ | | | | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | • | - | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | - | - | _ | _ | _ | _ | _ | _ | _ | _ | | _ | i | C | r | ι, | F | ۱p | p | li | Ca | at | 10 | ns | S | | | | | | | _ | |
| UNIT III | DIVIDE A | | | | | | | | | | | | _ | | | | | | | _ | _ | _ | _ | _ | | | | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | - | _ | _ | _ | _ | _ | _ | _ | _ | _ | | _ | | | _ | | _ | | _ | _ | | _ | | L | _ | | | H | | | ır | 'S | ; |
| Divide and C | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Huffman Tre | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | C |) | ٤ | 3 | ŢĬ | r | 8 | a | 11 | r | r | 1 | n | n | 1 | n | ٤ | 5 | V | /1 | t | h | 1 | A | ιl | g | 01 | it | h | m | 1 4 | 4r | ıa | ŀ | ys | 1S | , – | _ | | | | |
| Graph – Wai | | | | | | _ | _ | _ | | | | _ | | | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | | | | _ | _ | _ | _ | _ | _ | _ | _ | _ | C | ie | 21 | 1 | t | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | | _ | | _ | | _ | _ | | _ | _ | | | | | |
| UNIT IV | GREEDY | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | _ | | | | | | _ | | | | | | | | | _ | | | | _ | | | | | | _ | _ | | _ | _ | _ | _ | _ | _ | | _ | | _ | Ļ | | | | H | | | lľ | ·S | <u>;</u> |
| Prim's Algo | | skal | ıl's | 'S | S | S | S | I | 4 | Į | g | O | r | 1 | it. | h | 11 | 1 | r | 1 | S | | - | - | Ľ |)1 | 11 | k | S | tı | 8 | ı' | S | • | F | ١ | Į | g | 0 | r | 1 | t | ł | h | 11 | n | r | 1 | 15 | S | • | | - | - | | Ί | .] | 1 | e | | SI | :8 | ιt |). | le | • | Ν | 1 | ar | ri | a | ge | ٠. | r | oł | bΙ | lei | m | ι – | _ | | | | |
| Algorithm A | | | | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | 7 | | | _ | _ | _ | | _ | _ | _ | _ | _ | _ | _ | _ | • | | _ | _ | _ | | _ | _ | _ | _ | _ | _ | - | _ | ~ | | | | | | | | | | | _ | | | | _ | _ | _ | | _ | | | | | Т | | _ | _ | _ | | | | _ | |
| UNIT V | ALGORI | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | | | | _ | _ | | | | _ | | _ | _ | _ | _ | | _ | _ | L | _ | | | H | | | lľ | S | , |
| Algorithm A | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| Graph Color | rıng – Bran | ch a | ano | n | n | 10 | ıC | d | J | В | O |)(| u: | n | ıc | d | L | - | - | -] | L |) | e | С | 18 | 510 | O1 | n | | I | r | e | e | , | - | | '. | Ľ | ra | ľ | V | 76 | e | | I. | ľ | 1 | 1] | ľ | 1 | 1 2 | 2 | 5 | | , | S | a | l | e | S | n | 18 | 1 | n | | P | 'n | O | bl | eı | m | | - | K | n | aj | ps | sa | Ck | K | | | | |
| Problem | | | | | | | _ | | | | | _ | | _ | _ | _ | _ | _ | _ | _ | _ | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | | | | | | | | | _ | | _ | _ | _ | ., | _ | _ | _ | | Т | | | _ | _ | _ | | | _ | T T | _ | _ | _ |
| | DEADING | CTC | TCN/ | 74.7 | N / | | _ | | _ | | • | _ | _ | _ | _ | _ | _ | _ | _ | _ | _ | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | | L | Ĺ |) | L | A] | <u></u> | <u>:</u> | L | _ | | 4 | <u>5</u> | ŀ | 1(| O | U |) li | K | 2 | - |
| FURTHER | | | | | | | | | | | | | | | | | _ | _ | _ | _ | _ | _ | _ | _ | h | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | | | | | | | | | _ | | | | _ | | _ | | — | | | | | | _ | | | | | | | _ | |
| | 1. De 2. Ne | | | | | | | | | | | | | | _ | - | _ | - | _ | | | | | C | 11. | , | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | | | _ | _ | _ | _ | _ | _ | _ | _ | | _ | _ | | _ | _ | | | | _ | _ |
| COURSE O | | two | ork | rĸ | ſK | <u>K</u> | K1 | | ng | g | 1 | p | r | 0 | <u>)(</u> | <u>D</u> . | 11 | E | 3] | 1 | n | 18 | 3 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | | | | | | | | | _ | | | | _ | _ | _ | | _ | | | | | _ | _ | | | | | | | _ | |
| COURSE O | After comp | | ion | n | L | _ | _ | _ | £ | + | <u>_</u> | _ | _ | _ | _ | _ | _ | _ | _ | | _ | _ | | C | 41 | 10 | 10 | | . 1 | . , | • | • | 11 | | h | _ | _ | <u></u> | h | 1, | _ | _ | + | | _ | _ | | | | | | _ | | | | | | | | | | _ | | | | _ | | _ | | — | | | | | | _ | | | | | | | _ | |
| CO1 | Understand | | | | | | | | | | | | | | | | | | | | | | | | | | | | - | | | - | | | - | - | | | | | - | - | - | - | - | - | - | | | .; | | + | ı | | • | n | | | | | | - | | | | - | - | _ | _ | | _ | _ | _ | | _ | _ | | | | | | | - | _ |
| CO2 | Explain va | | | | | | | | | | | _ | _ | | | | | _ | _ | _ | _ | _ | _ | _ | а | 0 | и | ι | - | <i>-</i> ι | u | 1 | C | S | | a | 11 | u | ١. | _ | 1 | 1 | 1 2 | ٤ | 5 ' | · | , | '1 | _ | 1 | | ι. | 1. | 1. | 1. | 11 | S. | | | | | _ | | | | _ | _ | _ | _ | | _ | _ | _ | | | _ | | | | | | | _ | _ |
| CO3 | Apply Div | | | | | | | | | | | | | | | | | _ | _ | _ | _ | _ | _ | _ |) t | 711 | 10 | r | n | : | , | r | ۱,1 | •• | | ~ | r | 2 | n | 31 | n | n | | • | 1 | 1 | | _ | ` | | 1 | r | r | ٠. | _ | ıt | h | _ | ` | 4 | f | _ | | c | _ | 1. | . 7. | _ | 4 | f | F_ | rc | | t 1 | | <u>_</u> | hl | <u></u> | m | 10 | | | _ | |
| CO4 | Apply Gre | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | 3 | U | _ | <u>v </u> | _ | u | | | 10 | /1. | · | 71 | <u></u> | U | 10 | 111 | 10 | • | | - | |
| CO5 | Analysis v | | | | | | | | | | | | | | | | | _ | _ | _ | _ | _ | _ | _ | | | | _ | - | | | - | | | - | - | | _ | _ | | - | - | - | - | - | - | - | - | - | - | • | - | - | - | | _ | - | - | - | | ٥ | _ | | | | _ | _ | _ | _ | | _ | _ | _ | | | _ | | | | | | | _ | _ |
| REFERENC | | arrot | ous | 13 | 13 | 3 | _ | _ | 11 | <u>8</u> | <u>,</u> C | | . 1 | I L | .1. | 11 | | 1. | 1, | 3 | | u | ıc |)1 | 11 | Š | • | | | 1 | _ | u | د. | _ | ١, | <u> </u> | Ч | _ | ٥ | | a | | | - | _ | 1 | _ | | | 1 | .1 | | _ | · L | LI | .1' | _ | _ | 11 | , | | _ | | | | _ | _ | _ | _ | | _ | _ | _ | | | _ | | | | | | | _ | _ |
| 1. Mark Alle | | ata | ı St | St | St | t | tı | rı | u | c1 | tı | u | r | e | S | 3 | г | a | r | 1 | d | l | ŀ | ١ | <u>lę</u> | gc | r | i | ŀ | 11 | n | | Δ | \ 1 | 1 | a | 1 | y | S | 15 | S | | i | 1 | n | 1 | | (| C | 7 | ,- | - | + | - | + | ,; | , |] | P | e | a | r | S | 0 | n | 1 | E | d | u | _ ca | ıti | 0 | n | 2 | 0 | 1، | 4 | | | | | | | |
| 2. Thomas | H. Corme | en, | С | C | C. | ٦J | ŀ | h | a | rl | lε | 25 | S | | - | F | E | 3. | | | _ | I | _ | e: | is | e | rs | 36 |)] | 1 | | | F | ? | 0 | r | 18 | ıl | d | l | | | I | | _ | | .] | F | ₹ | | i | 1 | V | Έ | e | S | t. | , | | (| 7 | li | f | f | o | r | d | | S | te | :i | n, | | "] | n | tı | o | d١ | ac | et | tie | 0 | n | _ |
| toAlgorithms | s", Second E | Editio | tion | on | on | n | n | ı, | N | V | [c | C; | g | r | a | ľ | Ŋ | V | 7 | ŀ | Н | Ιi | il | 1, | , 2 | 2(|)] | 1 | 2 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |
| 3. Reema Th | areja, "Data | Stru | ruc | ıc | ıc | c | cí | tı | uı | re | 25 | S | Ţ | U | Js | Si | 11 | r | 1 | g | 5 | (| \Box | ,, | , | C |) X | đ | ì |)1 | Ċ | | Į | J | n | i | V | e | r | S | 1 | t | 3 | y | 7 |] | F |) | 1 | r | | е | :5 | S | S | , | 1 | 2 | 0 | 1 | 1 | | | | | | | | | | | | | | | | | | | | | | | |
| 4. Aho, Hop | croft and Ul | lmar | an, | ı, | ı, | , | , ' | " | ·L |)2 | at | ta | a | 5 | S | st | tr | rı | u | 10 | C1 | tı | u | r | es | S | aı | n | d | | 4 | 1 | g | ;C |)1 | i | ť | h | n | 1 | S | , | ,, | , | , |] | F | P |) | e | 2 | Е | ı | r | 3 | SC |) | n | | E | d | lu | | | | | | | | | | | | | | | | | | | | | _ | |
| 5. Michael T | | | | | | | | | | | Γ | a | 11 | r | 18 | a | ιS | S | S | i | г | ı, | , | Ι |)2 | ı | /i | d | | ١ | 1 | 0 | u | ır | 11 | ٠, | • | .6 | C |); | a | t | : | a | ι | | 5 | S | 5 | t | 1 | r | ι | l | C | t | ι | 1 | (| 25 | | a | n | C | l | F | ١ | ξ | ,0 | rit | tŀ | ın | 18 | iı | 1 | C | + | + | " | , | 8 | ßt | h | l |
| Edition, Wile | • | s, 20 | 201 |)1 |)1 | 1 | 1 | 4 | ١. | | | | | _ | | _ | _ | _ | _ | _ | _ | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | _ | | | | _ | _ | _ | | | | | | | | | | | | | | | _ | |
| 6. nptel.ac.in | / | | | | | | | | | | | | | _ | | _ | _ | | | _ | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | |

| 1902IT351 | | DATA STRUCTURES AND ALGORITHMS LAB | L | T | P | C |
|-----------|-------|------------------------------------|---|---|---|---|
| | | | 0 | 0 | 2 | 1 |
| PREREQUIS | SITE: | Programming using C | | | | |

LIST OF EXPERIMENTS:

MODULE 1:

- 1. Implement Array ADT
- 2. Write the program to perform Linked List, Stack and Queue Operations
- 3. Write the program to implement Tree Traversal operations
- 4. Write the program to implement sorting operations
- 5. Write the program to implement searching operations

MODULE 2:

- 1. Implement Tower of Honoi Problem using recursion
- 2. Implement Fibonacci number generation using recursion
- 3. Implement minimum spanning tree using Prim's, Kruskal's Algorithms
- 4. Write program to implement all the functions of a dictionary (ADT) using hashing.
- 5. Given the sequence of integers 5 9 1 7 4 3 2 0 manually arrange this sequence in ascending order using the three "'elementary"' sorting methods: insertion sort, bubblesort and selection sort, showing at each step the new configuration of the sequence. How many comparisons and how many element moves were used by each method? Which is the best performing method for sorting this array of integers? Which would be the worst arrangement of this sequence?

Hardware: Standalone desktops 30 Nos
Software: Turbo C++ compiler or equivalent

TOTAL: 30 HOURS

| 1902IT302 | COMPUTER ORGANIZATION AND ARCHITECTURE | L | T | P | C |
|---|--|---|---|---|-------------------------------|
| | | 3 | 0 | 0 | 3 |
| Course Objec | | | | | |
| | 1. To make students understand the basic structure and operation of digit | tal cor | nput | er. | |
| | 2. To study the concepts of pipelining. | | | | |
| | 3. To expose the students to the concept of parallelism | 1 | | | |
| | 4.To familiarize the students with hierarchical memory system including c | cache | | | |
| TT *4 T | memories and virtual memory. | | | Λ.Τ. | т |
| Unit I | STRUCTURE OF COMPUTERS & MACHINE INSTRUCTION | 337 - 11 | 10 | | lours |
| | Technologies for building Processors and Memory, Performance, The Power | | | | |
| | Hardware, Operands of the Computer Hardware, Signed and Unsignednut the Computer, Logical Operations, Instructions for MakingDecisions, Supp | | | | |
| | dware, Communicating with People. | orung | ; F10 | cedui | .es III |
| Unit II | PROCESSING UNIT | | | 9 H | lours |
| | sing for 32-Bit Immediate and Addresses, Parallelism and Instruction | ie. Sv | nchr | | |
| | nd Starting a Program, Addition and Subtraction, Multiplication, Divisi | | | | |
| | d Computer Arithmetic: Sub word Parallelism, Real Stuff: Streaming SIMD | | | | omi, |
| | Vector Extensions in x86. | Litton | | , | |
| Unit III | PIPELINING | | | 9 H | lours |
| | Conventions, Building a Datapath, A Simple Implementation Schem | e. An | 1 OV | | |
| | belined Datapath and Control, Data Hazards: Forwarding versus Stalling, Co. | | | | ,, 01 |
| | arallelism via Instructions, Real Stuff: The ARM Cortex – A8 and Intel Core | | | | |
| | Instruction –Level Parallelism and Matrix Multiply. An Introduction to Digi | | | , | |
| • | | | _ | | |
| Using a Hardw | vare Design Language to Describe and Model a Pipeline. | | | | |
| Using a Hardw Unit IV | ware Design Language to Describe and Model a Pipeline. MEMORY | | | 9 H | lours |
| Unit IV | | ce, dep | oenda | | lours |
| Unit IV Memory Techi memory hierar | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory | ry Hie | rarcl | able 1y, | |
| Unit IV Memory Techn memory hierar Using a Finite- | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory-State Machine to Control a Simple Cache, Parallelism and Memory Hierard | ry Hie chy: R | rarch edun | able 1y, 1dant | |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory-State Machine to Control a Simple Cache, Parallelism and Memory Hierard pensive Disks, Advanced Material: Implementing Cache Controllers, Real State Machine to Control a State Machine | ry Hie chy: R Stuff: T | rarch edun The A | able ny, idant ARM | |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory-State Machine to Control a Simple Cache, Parallelism and Memory Hierarch pensive Disks, Advanced Material: Implementing Cache Controllers, Real Stantel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material | ry Hie chy: R Stuff: T | rarch edun The A | able 1y, idant ARM y. | |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory-State Machine to Control a Simple Cache, Parallelism and Memory Hierard pensive Disks, Advanced Material: Implementing Cache Controllers, Real Statel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE | ry Hie chy: R Stuff: T rix Mu | erarch edun The A | able ny, idant ARM y. 9 H | lours |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory-State Machine to Control a Simple Cache, Parallelism and Memory Hierarchenister Disks, Advanced Material: Implementing Cache Controllers, Real Stantel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE ge and Dependability-RAID levels-hardware multi threading- | ry Hie chy: R Stuff: T rix Mu | erarch edun The A | able ny, idant ARM y. 9 H | |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory-State Machine to Control a Simple Cache, Parallelism and Memory Hierarchensive Disks, Advanced Material: Implementing Cache Controllers, Real Stantel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE ge and Dependability-RAID levels-hardware multi threading-rocessors-Multiprocessors network topologies. | ry Hie chy: R Stuff: T rix Mu | edun Edun The A Iltipl | able ny, idant ARM y. 9 H | lours ssage |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipr | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory-State Machine to Control a Simple Cache, Parallelism and Memory Hierard pensive Disks, Advanced Material: Implementing Cache Controllers, Real State Intel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE ge and Dependability-RAID levels-hardware multi threading-rocessors-Multiprocessors network topologies. TOTAL: | ry Hie chy: R Stuff: T rix Mu | edun Edun The A Iltipl | able ny, idant ARM y. 9 H | lours |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory-State Machine to Control a Simple Cache, Parallelism and Memory Hierarch pensive Disks, Advanced Material: Implementing Cache Controllers, Real Stantel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Matrice DISK STORAGE ge and Dependability-RAID levels-hardware multi threading-rocessors-Multiprocessors network topologies. TOTAL: | ry Hie chy: R Stuff: T rix Mu | edun Edun The A Iltipl | able ny, idant ARM y. 9 H | lours ssage |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipr Further Read | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory-State Machine to Control a Simple Cache, Parallelism and Memory Hierarchenister Disks, Advanced Material: Implementing Cache Controllers, Real Stantel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE ge and Dependability-RAID levels-hardware multi threading-rocessors-Multiprocessors network topologies. TOTAL: ling: 1. Introduction to Multi Core Programming 2. Working principles of Intel and AMD Processor | ry Hie chy: R Stuff: T rix Mu | edun Edun The A Iltipl | able ny, idant ARM y. 9 H | lours ssage |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipr | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory State Machine to Control a Simple Cache, Parallelism and Memory Hierarchensive Disks, Advanced Material: Implementing Cache Controllers, Real State Intel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE ge and Dependability-RAID levels-hardware multi threading-rocessors-Multiprocessors network topologies. TOTAL: ling: 1. Introduction to Multi Core Programming 2. Working principles of Intel and AMD Processor Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE ge and Dependability-RAID levels-hardware multi threading-rocessors-Multiprocessors network topologies. TOTAL: | ry Hie chy: R Stuff: T rix Mu | erarch edun Γhe A ultipl | able ny, idant ARM y. 9 H | lours ssage |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipr Further Read | memory mologies, the Basics of Caches, Measuring and Improving Cache Performance chy, Virtual Machines, Virtual Memory, A Common Framework for Memory State Machine to Control a Simple Cache, Parallelism and Memory Hierarc pensive Disks, Advanced Material: Implementing Cache Controllers, Real State Intel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE ge and Dependability-RAID levels-hardware multi threading- rocessors-Multiprocessors network topologies. TOTAL: Ing: 1. Introduction to Multi Core Programming 2. Working principles of Intel and AMD Processor Memory Mem | ry Hie chy: R Stuff: T rix Mu | erarch edun Γhe A ultipl | able ny, idant ARM y. 9 H | lours ssage |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipr Further Read | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory-State Machine to Control a Simple Cache, Parallelism and Memory Hierarchenister, Parallelism and Memory Hierarchenister, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in Memory Hierarchies, Going Faster: Cache Blocking and Matrice Intel Core in | ry Hie chy: R Stuff: T rix Mu | erarch edun Γhe A ultipl | able ny, idant ARM y. 9 H | lours ssage |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipr Further Read | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory State Machine to Control a Simple Cache, Parallelism and Memory Hierarch pensive Disks, Advanced Material: Implementing Cache Controllers, Real Standard Intel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE ge and Dependability-RAID levels-hardware multi threading-rocessors-Multiprocessors network topologies. TOTAL: Ing: 1. Introduction to Multi Core Programming 2. Working principles of Intel and AMD Processor mes: After completion of the course, Student will be able to CO1: Understand the concepts of structure of computers and machine instruction. CO2: Explain the concepts of processing units CO3: Design and analyze pipelined control units | ry Hie chy: R Stuff: T rix Mu | erarch edun Γhe A ultipl | able ny, idant ARM y. 9 H | lours ssage |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipr Further Read | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance Chy, Virtual Machines, Virtual Memory, A Common Framework for Memory State Machine to Control a Simple Cache, Parallelism and Memory Hierarch pensive Disks, Advanced Material: Implementing Cache Controllers, Real State Intel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE ge and Dependability-RAID levels-hardware multi threading rocessors-Multiprocessors network topologies. TOTAL: ling: 1. Introduction to Multi Core Programming 2. Working principles of Intel and AMD Processor mes: After completion of the course, Student will be able to CO1: Understand the concepts of structure of computers and machine instate CO2: Explain the concepts of processing units CO3: Design and analyze pipelined control units CO4: Evaluate performance of memory systems | ry Hie chy: R Stuff: T rix Mu cluste | erarch edun Γhe A ultipl | able ny, idant ARM y. 9 H | lours ssage |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipr Further Read Course Outco | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance, Virtual Machines, Virtual Memory, A Common Framework for Memory State Machine to Control a Simple Cache, Parallelism and Memory Hierarch pensive Disks, Advanced Material: Implementing Cache Controllers, Real Standard Intel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE ge and Dependability-RAID levels-hardware multi threading-rocessors-Multiprocessors network topologies. TOTAL: Ing: 1. Introduction to Multi Core Programming 2. Working principles of Intel and AMD Processor mes: After completion of the course, Student will be able to CO1: Understand the concepts of structure of computers and machine instruction. CO2: Explain the concepts of processing units CO3: Design and analyze pipelined control units | ry Hie chy: R Stuff: T rix Mu cluste | erarch edun Γhe A ultipl | able ny, idant ARM y. 9 H | lours ssage |
| Unit IV Memory Techn memory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipr Further Read Course Outco | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance Techy, Virtual Machines, Virtual Memory, A Common Framework for Memory State Machine to Control a Simple Cache, Parallelism and Memory Hierarch pensive Disks, Advanced Material: Implementing Cache Controllers, Real State Intel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE The pensive Disks, Advanced Material: Implementing Cache Controllers, Real State Intel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE TOTAL: TOTAL: Ing: 1. Introduction to Multi Core Programming 2. Working principles of Intel and AMD Processor TOTAL: CO1: Understand the concepts of structure of computers and machine instructions. CO2: Explain the concepts of processing units CO3: Design and analyze pipelined control units CO4: Evaluate performance of memory systems CO5: Understand disk storage and apply RAID concepts in real time problems. | ry Hiechy: R Stuff: Trix Mu cluste ruction | rarch edun Fhe A iltipl | able ny, dant ARM y. 9 H mes | lours ssage |
| Unit IV Memory Technomemory hierar Using a Finite-Arrays of Inex Cortex-A8 and Unit V Disk Storag passing multipr Further Read Course Outco References: 1. David | memory mologies, the Basics of Caches, Measuring and Improving Cache Performance rechy, Virtual Machines, Virtual Memory, A Common Framework for Memory State Machine to Control a Simple Cache, Parallelism and Memory Hierarch pensive Disks, Advanced Material: Implementing Cache Controllers, Real State Intel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE ge and Dependability-RAID levels-hardware multi threading- rocessors-Multiprocessors network topologies. TOTAL: Ing: 1. Introduction to Multi Core Programming 2. Working principles of Intel and AMD Processor mes: After completion of the course, Student will be able to CO1: Understand the concepts of structure of computers and machine instruction. CO2: Explain the concepts of processing units CO3: Design and analyze pipelined control units CO4: Evaluate performance of memory systems CO5: Understand disk storage and apply RAID concepts in real time problem. d A. Patterson and John L. Hennessey, "Computer organization and definition of the course, "Computer organization and definition | ry Hiechy: R Stuff: Trix Mu cluste ruction lems esign, | rarch | able ny, dant ARM y. 9 H mes | lours ssage |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipm Further Read Course Outco References: 1. David Hardy | mes: After completion of the course, Student will be able to CO1: Understand the concepts of processing units CO2: Explain the concepts of processing units CO3: Design and analyze pipelined control units CO4: Evaluate performance of memory systems CO5: Understand disk storage and John L. Hennessey, "Computer organization and deware/Software interface", Morgan Kauffman / Elsevier, Fifth edition, | ry Hiechy: R Stuff: Trix Mu cluste ruction lems esign, 2014 | The | able ny, dant ARM y. 9 H mes | lours ssage lours |
| Unit IV Memory Techn memory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipr Further Read Course Outco References: 1. David Hardy 2. Carl 1 | memory mologies, the Basics of Caches, Measuring and Improving Cache Performance rechy, Virtual Machines, Virtual Memory, A Common Framework for Memory State Machine to Control a Simple Cache, Parallelism and Memory Hierarch pensive Disks, Advanced Material: Implementing Cache Controllers, Real State Intel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Material DISK STORAGE ge and Dependability-RAID levels-hardware multi threading- rocessors-Multiprocessors network topologies. TOTAL: Ing: 1. Introduction to Multi Core Programming 2. Working principles of Intel and AMD Processor mes: After completion of the course, Student will be able to CO1: Understand the concepts of structure of computers and machine instruction. CO2: Explain the concepts of processing units CO3: Design and analyze pipelined control units CO4: Evaluate performance of memory systems CO5: Understand disk storage and apply RAID concepts in real time problem. d A. Patterson and John L. Hennessey, "Computer organization and definition of the course, "Computer organization and definition | ry Hiechy: R Stuff: Trix Mu cluste ruction lems esign, 2014 | The | able ny, dant ARM y. 9 H mes | [ours |
| Unit IV Memory Techn memory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipr Further Read Course Outco References: 1. David Hardy 2. Carl I McGr 3. Willia | MEMORY | ry Hiechy: R Stuff: Trix Mu cluste ruction lems esign, 2014. | rarchedun The Aultipli rs- The Edi | able ny, dant ARM 9 H mes | Iours ssage Iours |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipm Further Read Course Outco References: 1. David Hardy 2. Carl I McGr 3. Willia Sixth 4. V.P. | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance Corby, Virtual Machines, Virtual Memory, A Common Framework for Memory State Machine to Control a Simple Cache, Parallelism and Memory Hierarch pensive Disks, Advanced Material: Implementing Cache Controllers, Real Stantel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Matric DISK STORAGE levels-hardware multi threading-rocessors-Multiprocessors network topologies. TOTAL: | ry Hiechy: R Stuff: Trix Mu cluste ruction lems esign, 2014, 7°, 5th | The Edit | able ny, dant ARM y. 9 H mes 45 H | Iours ssage Iours Tata nce , |
| Unit IV Memory Technomemory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipm Further Read Course Outco References: 1. David Hardy 2. Carl I McGr 3. Willia Sixth 4. V.P. Pearse | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance (Chy.) Virtual Machines, Virtual Memory, A Common Framework for Memory State Machine to Control a Simple Cache, Parallelism and Memory Hierarch pensive Disks, Advanced Material: Implementing Cache Controllers, Real State Intel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Matrolls (Cache Torona) | ry Hiechy: R Stuff: Trix Mu cluste ruction lems esign, 2014, 7°, 5th | The Edit | able ny, dant ARM y. 9 H mes 45 H | Tata |
| Unit IV Memory Techn memory hierar Using a Finite- Arrays of Inex Cortex-A8 and Unit V Disk Storag passingmultipr Further Read Course Outco References: 1. David Hardy 2. Carl I McGr 3. Willia Sixth 4. V.P. Pearse 5. Behro | MEMORY nologies, the Basics of Caches, Measuring and Improving Cache Performance Corby, Virtual Machines, Virtual Memory, A Common Framework for Memory State Machine to Control a Simple Cache, Parallelism and Memory Hierarch pensive Disks, Advanced Material: Implementing Cache Controllers, Real Stantel Core i7 Memory Hierarchies, Going Faster: Cache Blocking and Matric DISK STORAGE levels-hardware multi threading-rocessors-Multiprocessors network topologies. TOTAL: | ry Hiechy: R Stuff: Trix Mu cluste ruction lems esign, 2014, 7°, 5th | The Edit | able ny, dant ARM y. 9 H mes 45 H | Iours ssage Iours Tata nce , |

| 1003177303 | | DICITAL DRINGIDI EC AND DECICAL | | _ | | |
|--------------------|---------------|--|----------|--------|--------|-------|
| 1902IT303 | + | DIGITAL PRINCIPLES AND DESIGN | <u>L</u> | T 0 | P 0 | C |
| AIM. This | s to provide | the concepts of Digital principles, logic, conversion and desig | | • | | 3 |
| COURSE OBJ | _ | the concepts of Digital principles, logic, conversion and desig | n pro | cedu | les | |
| OURSE OD. | | to design digital circuits, by simplifying the Boolean functi | one | A 1co | givo | 0.00 |
| | | lesigns using PLDs, and writing codes for designing larger dig | | | _ | s all |
| UNIT I | | N ALGEBRA AND LOGIC GATES | gitai | syster | | ours |
| | | ems – Arithmetic Operations – Binary Codes – Boolean Alge | hro o | nd Ti | | |
| | | opplification of Boolean Functions using Karnaugh Map and Ta | | | | |
| | | d NOR Implementations. | aouia | tion i | VICTI | Jus – |
| UNIT II | | ATIONAL LOGIC | | | 0 H | ours |
| | | Analysis and Design Procedures – Circuits for Arithmetic | ic Or | erati | | |
| | | and Encoders – Multiplexers and Demultiplexers – Introduction | | | | |
| Models of C | | | tion t | O IIL | L. | IIDL |
| UNIT III | | ONOUS SEQUENTIAL LOGIC | | | 9 H | ours |
| | | atches and Flip Flops – Analysis and Design Procedures – S | State | Redu | | |
| | | Registers – Counters – HDL for Sequential Logic Circuits. | State | reac | | una |
| UNIT IV | | RONOUS SEQUENTIAL LOGIC | | | 9 H | ours |
| | | Asynchronous Sequential Circuits – Reduction of State and | l Flo | w Tai | | |
| | | ent – Hazards | 10 | ,, , | 0105 | |
| UNIT V | | AND PROGRAMMABLE LOGIC | | | 9 H | ours |
| | | ory Decoding – Error Detection and Correction – Programn | nable | Logi | | |
| | | ogic – Sequential Programmable Devices – Application | | | | |
| Circuits. | • | | • | | Ü | |
| | | TOTA | L: | 45 | 5 НО | URS |
| FURTHER | READING / | SEMINAR: | | | | |
| | 1. De | cision Tree Approach | | | | |
| | 2. Ne | tworking problems | | | | |
| COURSE O | | | | | | |
| | After comp | letion of the course, Student will be able to | | | | |
| CO1 | | different methods used for the simplification of Boolean fundamental | ctions | S | | |
| CO2 | | e fundamentals of VHDL / Verilog HDL | | | | |
| CO3 | Design and | implement combinational circuits | | | | |
| CO4 | | implement synchronous sequential circuits | | | | |
| CO5 | Design and | implement asynchronous sequential circuits | | | | |
| REFERENC | CES: | | | | | |
| 1. Morris Ma | ano M. and I | Michael D. Ciletti, "Digital Design", Pearson Education, 2015 | ·. | | | |
| 2. John F. W | akerly, "Dig | gital Design Principles and Practices", Seventh Edition, Pearson | on Ed | ucatio | on, 20 |)15 |
| 3. Charles H 2013. | . Roth Jr, "F | fundamentals of Logic Design", Fifth Edition – Jaico Publish | ing H | ouse, | , Mur | nbai, |
| | Givone "F | igital Principles and Design", Tata Mcgraw Hill, 2013. | | | | |
| | | l Electronics", Oxford University Press, 2010. | | | | |
| 7. http://npte | | 2 Diversition , Onlord Onitionly 11666, 2010. | | | | |
| ,. nup.//nptc | 1.40.111 | | | | | |

| 1902IT352 | DIGITAL PRINCIPLES AND DESIGN LAB | L | T | P | C |
|-----------|-----------------------------------|---|---|---|---|
| | | 0 | 0 | 2 | 1 |
| | | | | | |

LIST OF EXPERIMENTS:

- 1. Verification of Boolean Theorems using basic gates.
- 2. Design and implementation of combinational circuits using basic gates for arbitrary functions, code converters.
- 3. Design and implementation of combinational circuits using MSI devices: 4 bit binary adder / subtractor Parity generator / checker Magnitude Comparator Application using multiplexers
- 4. Design and implementation of sequential circuits: Shift –registers Synchronous and asynchronous counters
- 5. Coding combinational / sequential circuits using HDL.
- 6. Design and implementation of a simple digital system

Hardware: 1. Digital trainer kits 302. Digital ICs required for the experiments in sufficient numbers

Software: 1. HDL simulator

TOTAL: 30 HOURS

| 1902IT304 | 1 | PROBLEM SOLVING USING PYTHO | N | L | T | P | C |
|-------------|-------------|--|------------|---------|----------|--------|--------|
| | | | | 3 | 0 | 0 | 3 |
| PREREQU | | • • | | | | | |
| COLIDGE | | ming Languages | | | | | |
| COURSE | | | | | | | |
| | | o know the basics of problem solving | | | | | |
| | | o read and write simple Python programs. | | | | | |
| | | o develop Python programs with conditions, loops | and data | structi | ires. | | |
| | 4. T | o define Python functions and call them. | | | | | |
| | 5. T | o do input/output with files in Python. | | | | | |
| UNIT I | PROBI | EM SOLVING AND PYTHON INTRODUCTI | ON | | | 9 I | Hours |
| Problem s | solving te | chniques: Program development life-cycle – A | Algorithm | s – t | ouilding | g bloc | ks of |
| | | art- Pseudo Code-Illustrative problems. Introduct | | | | | |
| and its wor | rking, Syn | tax and Semantics | | | | | - |
| UNIT II | PYTHO | ON BASICS | | | | 9 F | Hours |
| Data Types | s, operator | rs, loops, Assignments and Expressions, Control Fl | ow Stater | nents. | | | |
| UNIT III | DATA | STRUCTURES AND FUNCTIONS | | | | 9 I | Hours |
| Lists-Tuple | es-Diction | aries-Functions and lambda expressions-Iterations | and Com | preher | sions. | | |
| UNIT IV | FILES, | MODULES AND Packages | | | | 9 I | Hours |
| Files and e | exception: | text files, reading and writing files, format operator | r; comma | ınd lin | e argui | ments, | errors |
| and except | ions, hand | ling exceptions, modules, packages | | | | | |
| UNIT V | CLASSI | ES OBJECTS And REGULAR EXPRESSIONS | | | | 9 I | Hours |
| Overview of | of OOPs t | erminology-class-inheritance-overloading-Regular | Expressi | | | | |
| | | | | T(| TAL: | 45 H(| OURS |
| Course Ou | | | | | | | |
| | | ourse, students will be able to, | | | | | |
| | | on code in variety of environments | | | | | |
| | | Python control flow construct | | | | | |
| | | tructures and functions using python | | | | | |
| • | | e, Modules and Packages concepts using Python | | | | | |
| | | wn classes and use existing python classes | | | | | |
| | | NG: Python for Data Science | | | | | |
| REFEREN | | | | | | | |
| | | own, "PYTHON: The Complete Reference", McGra | w Hill,200 |)1. | | | |
| 2. Na | ıomi R. Ce | der, The Quick Python Book, Second Edition, 2010 | | | | | |

- 2. Naomi R. Ceder, The Quick Python Book, Second Edition, 2010
- 3. Guido van Rossum and Fred L. Drake Jr, —An Introduction to Python Revised and updated for
- 4. Python 3.2, Network Theory Ltd., 2011.
- 5. John V Guttag, —Introduction to Computation and Programming Using Python", Revised and
- 6. expanded Edition, MIT Press, 2013
- 7. Robert Sedgewick, Kevin Wayne, Robert Dondero, —Introduction to Programming in Python: An
- 8. Inter-disciplinary Approach, Pearson India Education Services Pvt. Ltd., 2016.
- 9. Allen B. Downey,"Think Python: How to Think Like a Computer Scientist", 2nd edition,
- 10. Updatedfor Python 3,Shroff/O'Reilly Publishers, 2016 (http://greenteapress.com/wp/thinkpython/)
- 11. http://nptel.ac.in/

| 1902IT353 | PYTHON PROGRAMMING LAB | L | T | P | C |
|-----------|------------------------|---|---|---|---|
| | | 0 | 0 | 2 | 1 |

List of Experiments:

- 1. Study of key features of the Python language, intro to the Python IDE's
- 2. Play with Data types, keywords, conditional and control statements, looping, branching
- 3. Implement Python program concepts using List, Tuple and Dictionaries
- 4. Implement Functions using Python
- 5. Perform the following file operations using Python
 - a) Traverse a path and display all the files and subdirectories in each level till the Deepest level for a given path. Also, display the total number of files and subdirectories.
 - b) Read a file content and copy only the contents at odd lines into a new file.
- 6. Write a Python program to construct a linked list. Prompt the user for input. Remove any duplicate numbers from the linked list.
- 7. Perform Sorting and Searching using Python
- 8. Perform the following file operations using Python
 - a) Traverse a path and display all the files and subdirectories in each level till the deepest level for a given path. Also, display the total number of files and subdirectories.
- b) Read a file content and copy only the contents at odd lines into a new file
- 9. Perform exception handling using Python
- 10. Implement Python programming concepts using classes and objects
- 11. Using Regular Expressions, develop a Python program to
 - a) Identify a word with a sequence of one upper case letter followed by lower case letters.
 - b) Find all the patterns of "1(0+)1" in a given string.
 - c) Match a word containing 'z' followed by one or more o's. Prompt the user for input.
- 12. Devise a Python program to implement the Hangman Game.
- 13. Simulate bouncing ball using Pygame

Requirements:

Software:

Operating System: Windows /Linux operating system

Tool: Python 3.6 (or above) **IDE:** Pycharm, Spyder

TOTAL: 30 HOURS

Online Resource:

https://www.learnpython.org/

https://wiki.python.org/moin/BeginnersGuide/Programmers

https://www.python.org/about/gettingstarted/

https://www.javatpoint.com/python-tutorial

https://www.geeksforgeeks.org/python-programming-language/

| | | Approved in IV Academic Council | Mee | ting h | eld o | n 25-05 |
|------------------|--------|--|--------|--------|--------|---------|
| 1901GEX04 | | BIOLOGY FOR ENGINEERS | L | T | P | C |
| | | | 3 | 0 | 0 | 3 |
| | | | | | | |
| COURSE OBJI | ECTIVE | The objective of this course is to enable learners to under concepts of biology and its applications in engineering. | stand | the b | asic | |
| COURSE | Upon c | ompletion of this course, students will be able to | | | | |
| OUTCOMES: | 1. | Describe how biological observations of 18th Century | y tha | t lea | d to | major |
| | | discoveries. | | | | |
| | 2. | Classify biology based on morphological, biochemical and | l ecol | ogica | ıl mat | tters |
| | 3. | Describe the concepts of recessiveness and dominance | durin | g the | pass | sage of |
| | | genetic material from parent to offspring | | C | 1 | C |
| | 4. | Analyze biological processes at the reductionistic level | | | | |
| | 5. | Describe about all forms of life have the same buildin manifestations are as diverse as one can imagine | ig blo | ocks | and ; | yet the |
| | 6. | Classify enzymes and distinguish between different meaction. | echan | isms | of e | enzyme |
| | 7. | Describe DNA as a genetic material in the molecular transfer. | basi | s of | infor | mation |
| | 8. | Apply thermodynamic principles to biological systems. | | | | |
| | 9. | Classify microorganisms. | | | | |
| | 10. | Describe about bio-inspired engineering. | | | | |

Module I Biology Introduction and its Classification

7 Hours

Introduction to Biology, fundamental differences between science and engineering by drawing a comparison between eye and camera, Bird flying and aircraft. Exciting aspect of biology - need to study biology- Discussion about biological observations of 18th Century - major discoveries. Examples from Brownian motion and the origin of thermodynamics - original observation of Robert Brown and Julius Mayor.

Classification - morphological, biochemical or ecological. Hierarchy of life forms at phenomenological level. classification based on (a) cellularity- Unicellular or multicellular (b) ultrastructure- prokaryotes or eucaryotes. (c) energy and Carbon utilization -Autotrophs, heterotrophs, lithotropes (d) Ammonia excretion – aminotelic, uricoteliec, ureotelic (e) Habitata- acquatic or terrestrial (e) Molecular taxonomythree major kingdoms of life. Model organisms for the study of biology- E.coli, S.cerevisiae, D. Melanogaster, C. elegance, A. Thaliana, M. musculus

Module IIGenetics and Macromolecular analysis10 Hours

Genetics - Newton's laws to Physical Sciences"- Mendel's laws, Concept of segregation and independent assortment. Concept of allele. Gene mapping, Gene interaction, Epistasis. Meiosis and Mitosis - part of genetics. Concepts of recessiveness and dominance. Concept of mapping of phenotype to genes. Single gene disorders in humans. Complementation using human genetics.

Macromolecular analysis: analyses of biological processes at the reductionistic level Proteins- structure and function. Hierarch in protein structure. Primary secondary, tertiary and quaternary structure. Proteins as enzymes, transporters, receptors and structural elements.

Module III Biomolecules and Enzymes

10 Hours

Biomolecules - Molecules of life. monomeric units and polymeric structures. Sugars, starch and cellulose. Amino acids and proteins. Nucleotides and DNA/RNA. Two carbon units and lipids.

Enzymes - monitor enzyme catalyzed reactions. Enzyme catalyzereactions. Enzyme classification. Mechanism of enzyme action -two examples. Enzyme kinetics and kinetic parameters. RNA catalysis.

Information Transfer - The molecular basis of coding and decoding genetic information - universal Molecular basis of information transfer. DNA - genetic material. Hierarchy of DNA structure- from single stranded to double helix to nucleosomes. Concept of genetic code. Universality and degeneracy of genetic code. Gene in terms of complementation and recombination.

Module IV Metabolism and Microbiology

8 Hours

Metabolism: principles of energy transactions. Thermodynamics to biological systems. Exothermic and endothermic versus endergonic and exergoinc reactions. Concept of Keq and its relation to standard free energy. Spontaneity. ATP - energy currency. Breakdown of glucose to CO2 + H2O (Glycolysis and Krebs cycle) - synthesis of glucose from CO2 and H2O (Photosynthesis). Energy yielding and energy consuming reactions. Concept of Energy charge

Microbiology Concept of single celled organisms. Concept of species and strains. Identification and classification of microorganisms. Microscopy. Ecological aspects of single celled organisms. Sterilization and media compositions. Growth kinetics.

Module V Bio-inspired Engineering

10 Hours

Introduction to biologically-inspired designs (BID for Biomedical and Non-biomedical applications): Human-organs-on-chips; Muscular Biopolymers; Bio-optics; Nanostructures for Drug Delivery; Genetic Algorithms; Artificial neural networks; Swarm intelligence algorithms; Biosensors: role in medical diagnostics (Sensium digital plaster); environmental monitoring; Bio-filters; Bio-robotics; 3D Bio-printing; Self healing concrete.

| | | | Total: | 45 Hours |
|--------------------|--|---|---------------|------------------|
| REFERENCES: | | | | |
| | 1. Biology for Engin First Edition edition | neers, Rajiv Singal, CBS Publisheon (4 June 2019). | ers and Distr | ibutors Pvt Ltd; |
| | 2. Biology for Engin | neers, Wiley Editorial, Wiley (201 | 8). | |
| | 3. Principles of Soft edition (2018). | Computing, S. N. Sivanandam, S | . N. Deepa, | Wiley; Third |
| | 4. Computational Me 2012 edition (19 S | edicine: Tools and Challenges, Zl September 2012). | latko Trajano | oski, Springer; |
| | | s - E-Book: An Interprofessional aggers, Elsevier; 2 edition (Decen | | |
| | 6. Biology for Engin | neers, G.KSuraishkumar, Oxford | University 1 | Press |
| | 7. Biology for Engin | neers, Arthur T. Johnson, CRC Pre | ess | |

| 1902MCX02 | CONSTITUTION OF INDIA | L | T | P | C |
|--------------------|-----------------------|---|---|---|---|
| | | 2 | 0 | 0 | 0 |
| Course Cont | ent | | | | |

The Constitution of India is the supreme law of India. Parliament of India can not make any law which violates the Fundamental Rights enumerated under the Part III of the Constitution. The Parliament of India has been empowered to amend the Constitution under Article 368, however, it cannot use this power to change the "basic structure" of the constitution, which has been ruled and explained by the Supreme Court of India in its historical judgments. The Constitution of India reflects the idea of "Constitutionalism" – a modern and progressive concept historically developed by the thinkers of "liberalism" – an ideology which has been recognized as one of the most popular political ideology and result of historical struggles against arbitrary use of sovereign power by state. The historic revolutions in France, England, America and particularly European Renaissance and Reformation movement have resulted into progressive legal reforms in the form of "constitutionalism" in many countries.

The Constitution of India was made by borrowing models and principles from many countries including United Kingdom and America. The Constitution of India is not only a legal document but it also reflects social, political and economic perspectives of the Indian Society. It reflects India's legacy of "diversity". It has been said that Indian constitution reflects ideals of its freedom movement, however, few critics have argued that it does not truly incorporate our ownancient legal heritage and cultural values. No law can be "static" and therefore the Constitution of India has also been amended more than one hundred times. These amendments reflect political, social and economic developments since the year 1950. The Indian judiciary and particularly the Supreme Court of India has played an historic role as the guardian of people. It has been protecting not only basic ideals of the Constitution but also strengthened the same through progressive interpretations of the text of the Constitution. The judicial activism of the Supreme Court of India and its historic contributions has been recognized throughout the world and it gradually made it "as one of the strongest court in the world".

Course content

- 1. Meaning of the constitution law and constitutionalism
- 2. Historical perspective of the Constitution of India
- 3. Salient features and characteristics of the Constitution of India
- 4. Scheme of the fundamental rights
- 5. The scheme of the Fundamental Duties and its legal status
- 6. The Directive Principles of State Policy Its importance and implementation
- 7. Federal structure and distribution of legislative and financial powers between the Union and the States
- 8. Parliamentary Form of Government in India The constitution powers and status of the President of India
- 9. Amendment of the Constitutional Powers and Procedure
- 10. The historical perspectives of the constitutional amendments in India
- 11. Emergency Provisions: National Emergency, President Rule, Financial Emergency
- 12. Local Self Government Constitutional Scheme in India
- 13. Scheme of the Fundamental Right to Equality
- 14. Scheme of the Fundamental Right to certain Freedom under Article 19
- 15. Scope of the Right to Life and Personal Liberty under Article 21

TOTAL: 30 HOURS

| 1904GE351 | LIFE SKILLS: VERBAL ABILITY | L | T | P | C |
|-----------|-----------------------------|---|---|---|---|
| | | 0 | 0 | 2 | 1 |

Course Objectives:

The students should be made to:

- 1. To help students comprehend and use vocabulary words in their day to day communication.
- 2. To apply appropriate reading strategies for interpreting technical and non-technical documents used in job-related settings.
- 3. To ensure students will be able to use targeted grammatical structures meaningfully and appropriately in oral and written production.
- 4. To enable the students to arrange the sentences in meaningful unit and to determine whether constructions rely on active or passive voice

5. To Apply the principles of effective business writing to hone communication skills

| 5. To Apply the principles of effective business writing to none communication skills | | | | | |
|--|-----------------------------------|---------|--|--|--|
| Unit I | VOCABULARY USAGE | 6 Hours | | | |
| Introduction - Synonyms and Antonyms based on Technical terms - Single word Substitution | | | | | |
| Audio and video listening activity. | | | | | |
| Unit II | COMPREHENSION ABILITY | 6 Hours | | | |
| Skimming and Scanning – Social Science passages – Business and Economics passages – latest political and | | | | | |
| current event based passages – Theme detection – Deriving conclusion from passages | | | | | |
| Unit III | BASIC GRAMMAR AND ERROR DETECTION | 6 Hours | | | |
| Parallelism – Redundancy – Ambiguity – Concord - Common Errors – Spotting Errors – Sentence improvement – | | | | | |
| Error Detection FAQ in Competitive exams. | | | | | |
| Unit IV | REARRANGEMENT AND GENERAL USAGE | 6 Hours | | | |
| Jumble Sentences – Cloze Test - Idioms and Phrases – Active and passive voice – Spelling test. | | | | | |
| Unit V | APPLICATION OF VERBAL ABILITY | 6 Hours | | | |
| Business Writing - Business Vocabulary - Delivering Good / Bad News - Media Communication - Email Etiquette - Report Writing - Proposal writing - Essay writing - Indexing - Market surveying. | | | | | |

TOTAL 30 Hours

COURSE OUTCOMES:

On Completion of the course, the students should be able to

- CO1: Use new words in their day to day communication.
- CO2: Gather information swiftly while reading passages.
- CO3: Students are proficient during their oral and written communication.
- CO4: Rearrange the sentences and able to identify the voice of the sentence.
- CO5: Students use their knowledge of the best practices to craft effective business documents

REFERENCES:

- 1. Arun Sharma and Meenakshi Upadhyav, How to Prepare for Verbal Ability and Reading Comprehension for CAT, McGrawHill Publication, Seventh Edition 2017
- 2. R S Aggarwal and Vikas Aggarwal , Quick Learning Objective General English ,S.Chand Publishing House, 2017
- 3. Dr.K.Alex, Soft Skills, S.Chand Publishing House, Third Revise Edition, 2014
- 4. Raymond Murphy, Essential English Grammar in Use, Cambridge University press, New Delhi, Third Edition, 2007

ASSESSMENT PATTERN:

- 1. Two tests will be conducted (25 * 2) 50 marks
- 2. Five assignments will be conducted (5*10) 50 Marks